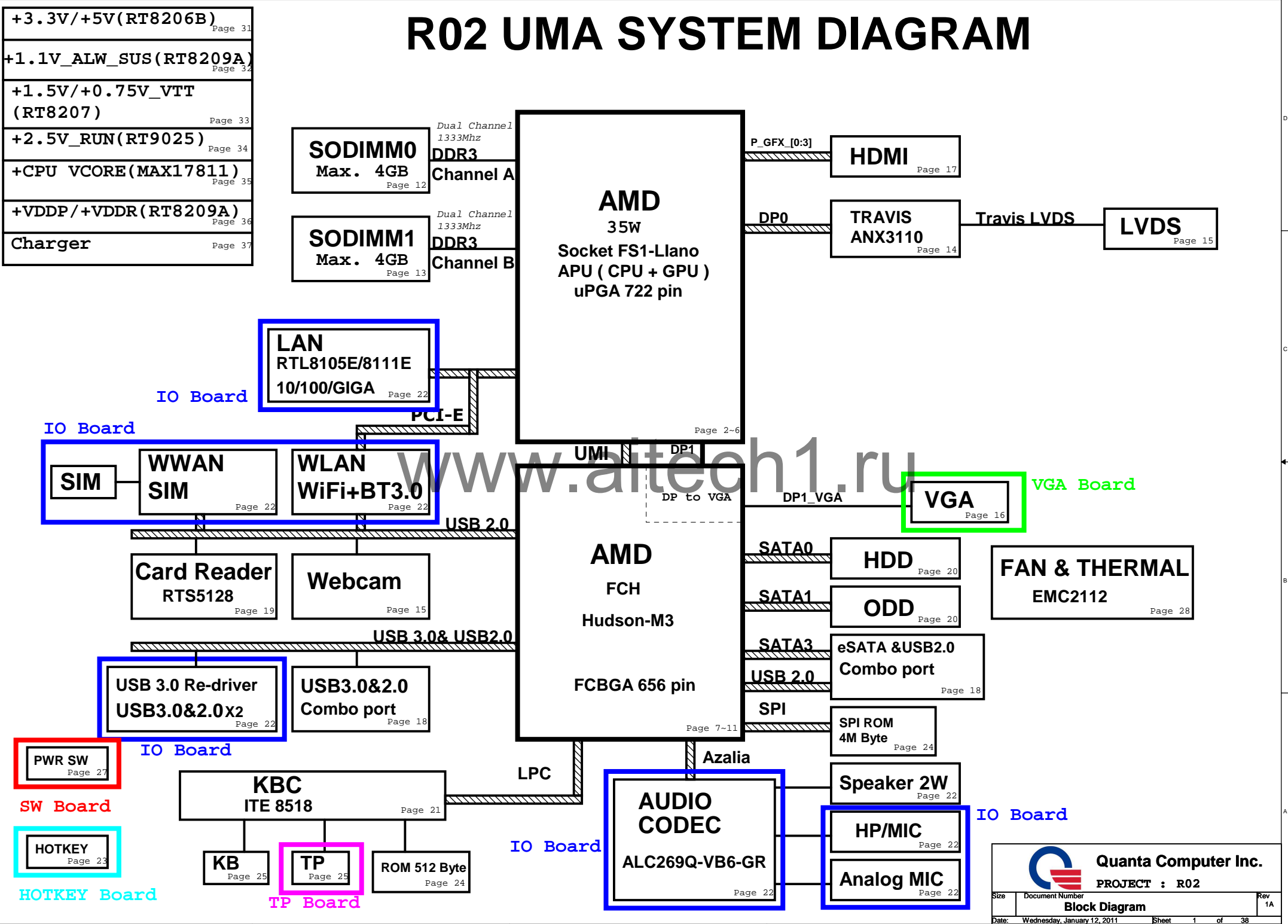
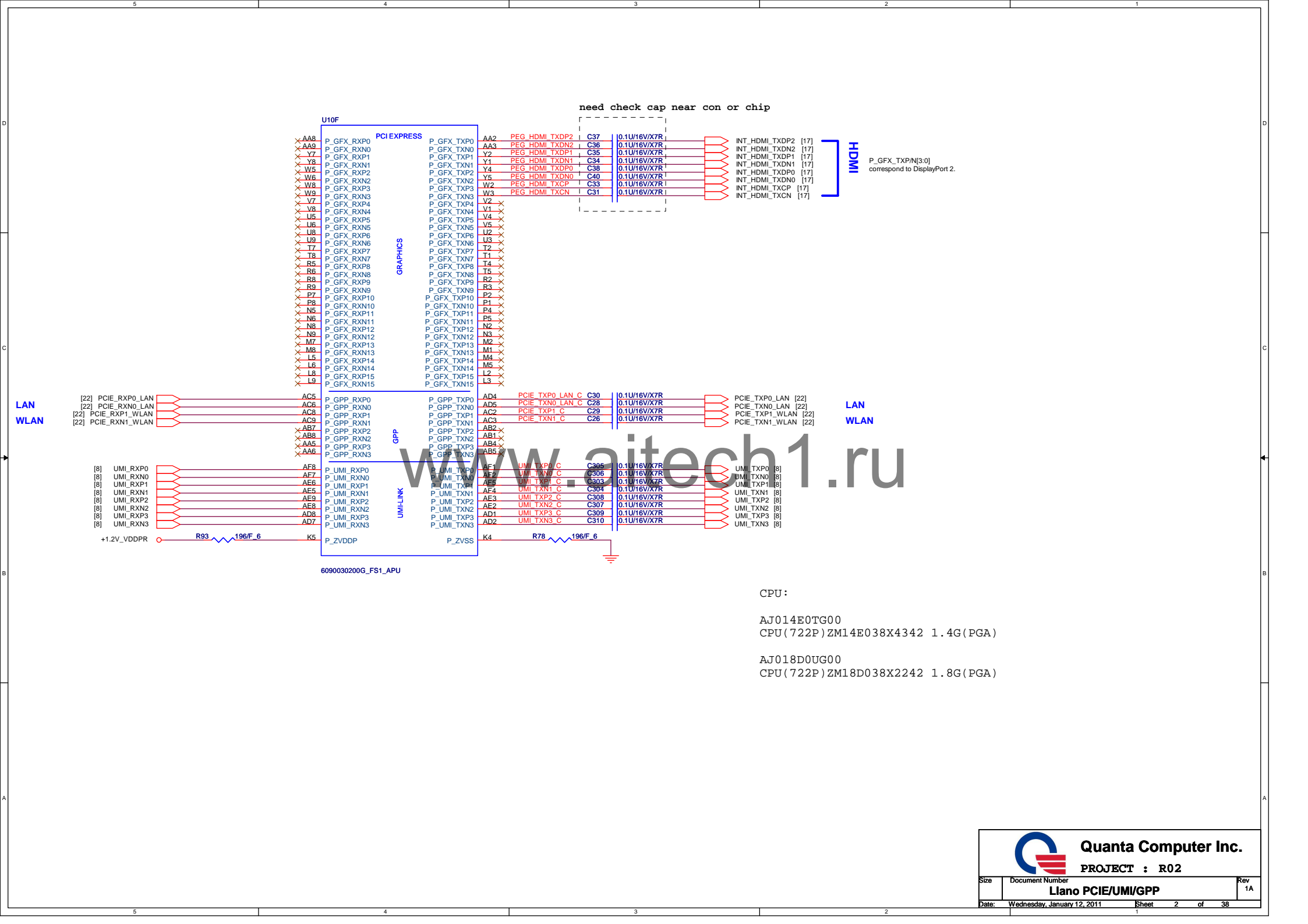
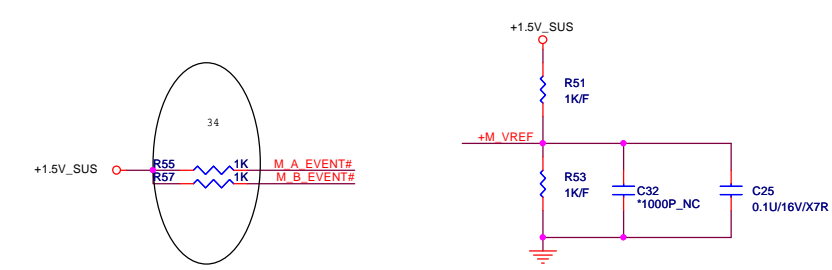
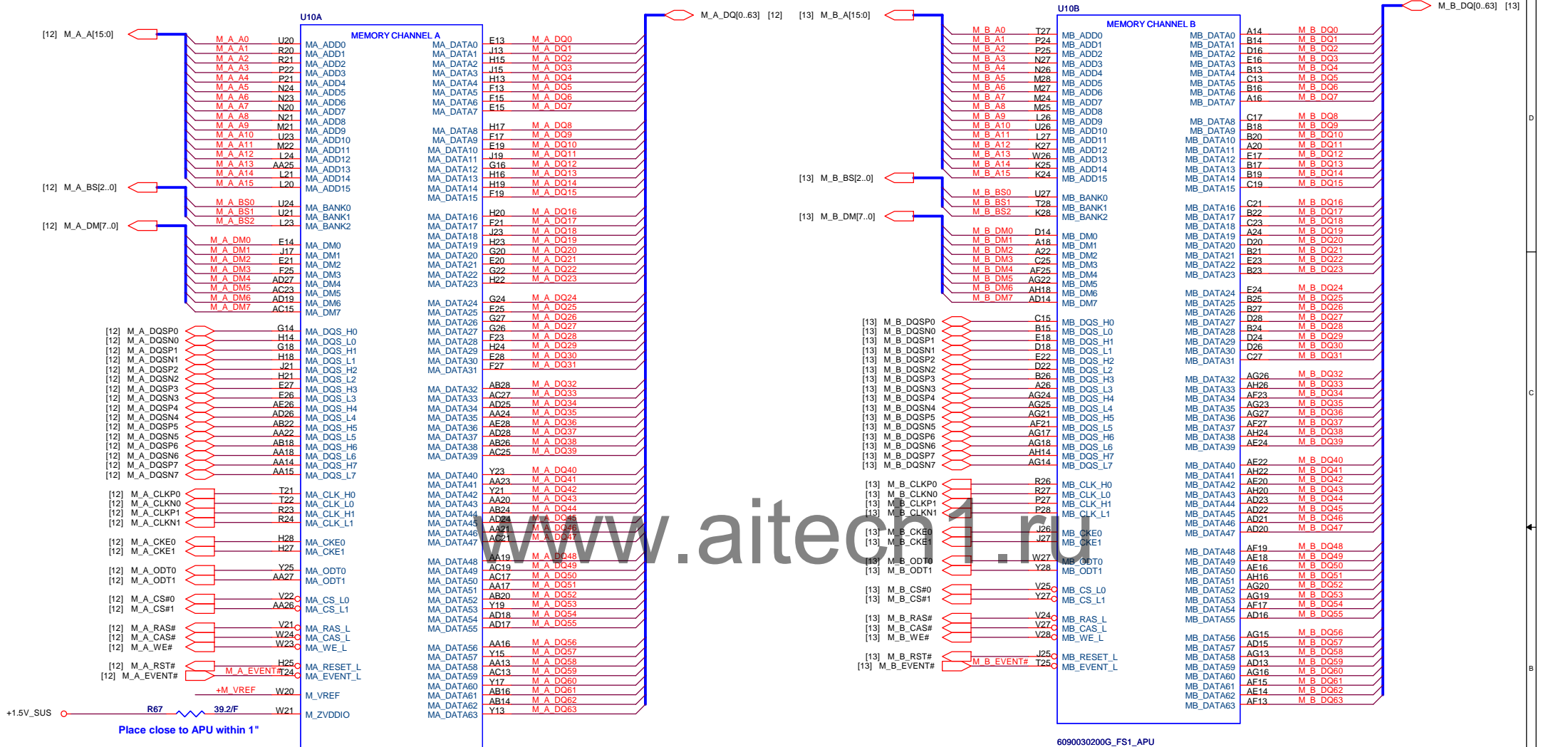


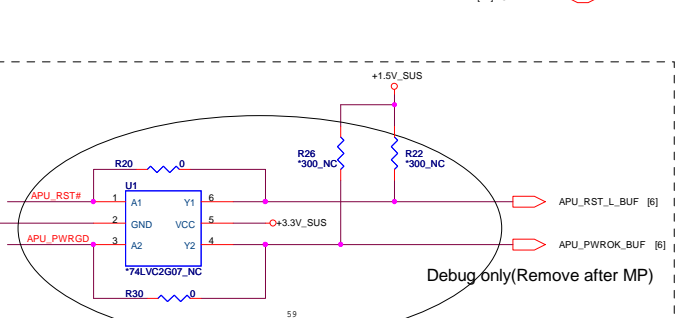
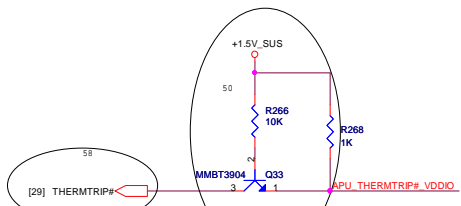
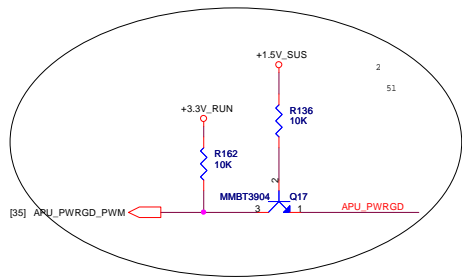
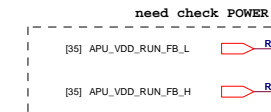
R02 UMA SYSTEM DIAGRAM







+1.5V_SUS



Debug only(Remove after MP)

Check list need add ESD part near con

APU I2C to FCH

APU to Travis IC

CRT I2C to FCH

INT_HPD_Q [14]

INT_LVDS_AUXP [14]

INT_LVDS_AUXN [14]

APU_VGA_AUXP [9]

APU_VGA_AUXN [9]

HDMI_SCL [17]

HDMI_SDA [17]

LVDS_HPD_Q [14]

APU_BLEN

APU_DIGON

APU_PLWPM [14]

APU_TEST10_PLLEST0 [8]

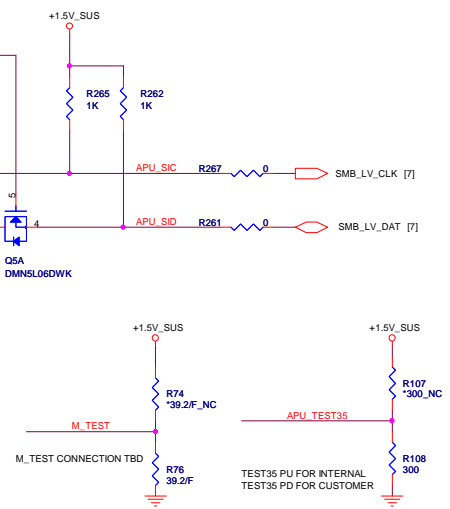
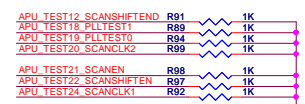
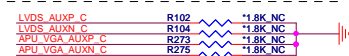
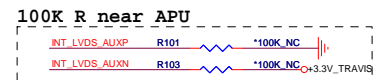
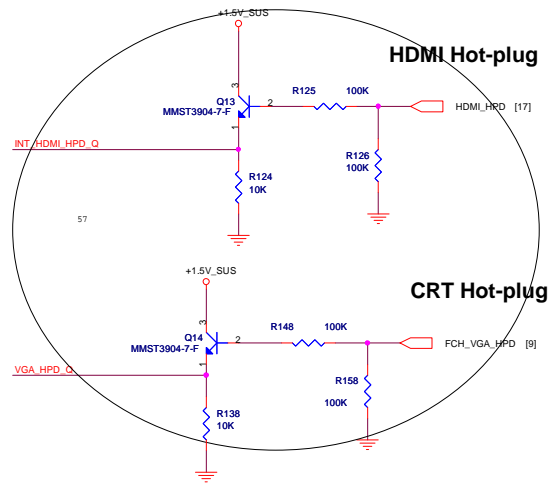
APU_TEST19_PLLEST0 [6]

+3.3V_ALW

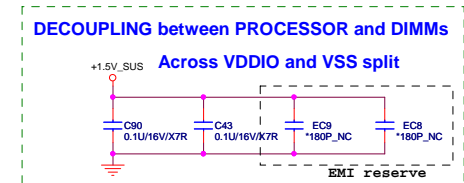
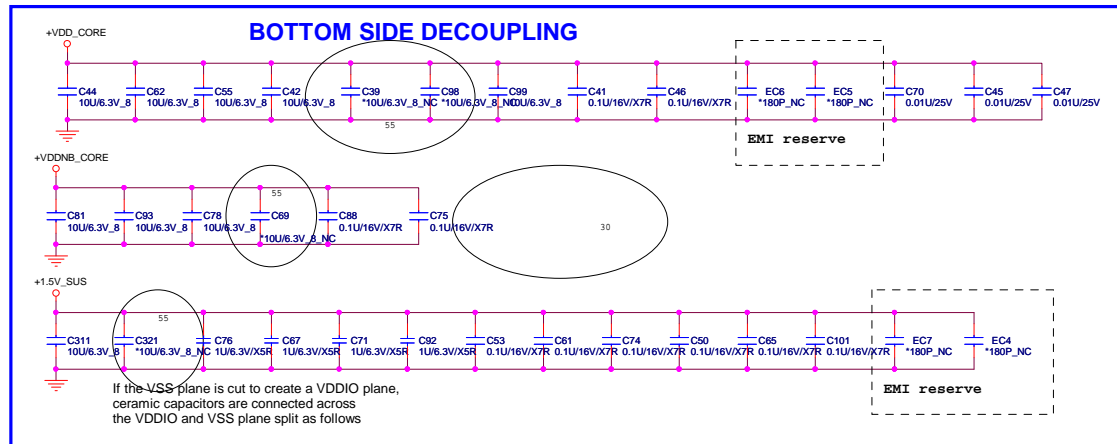
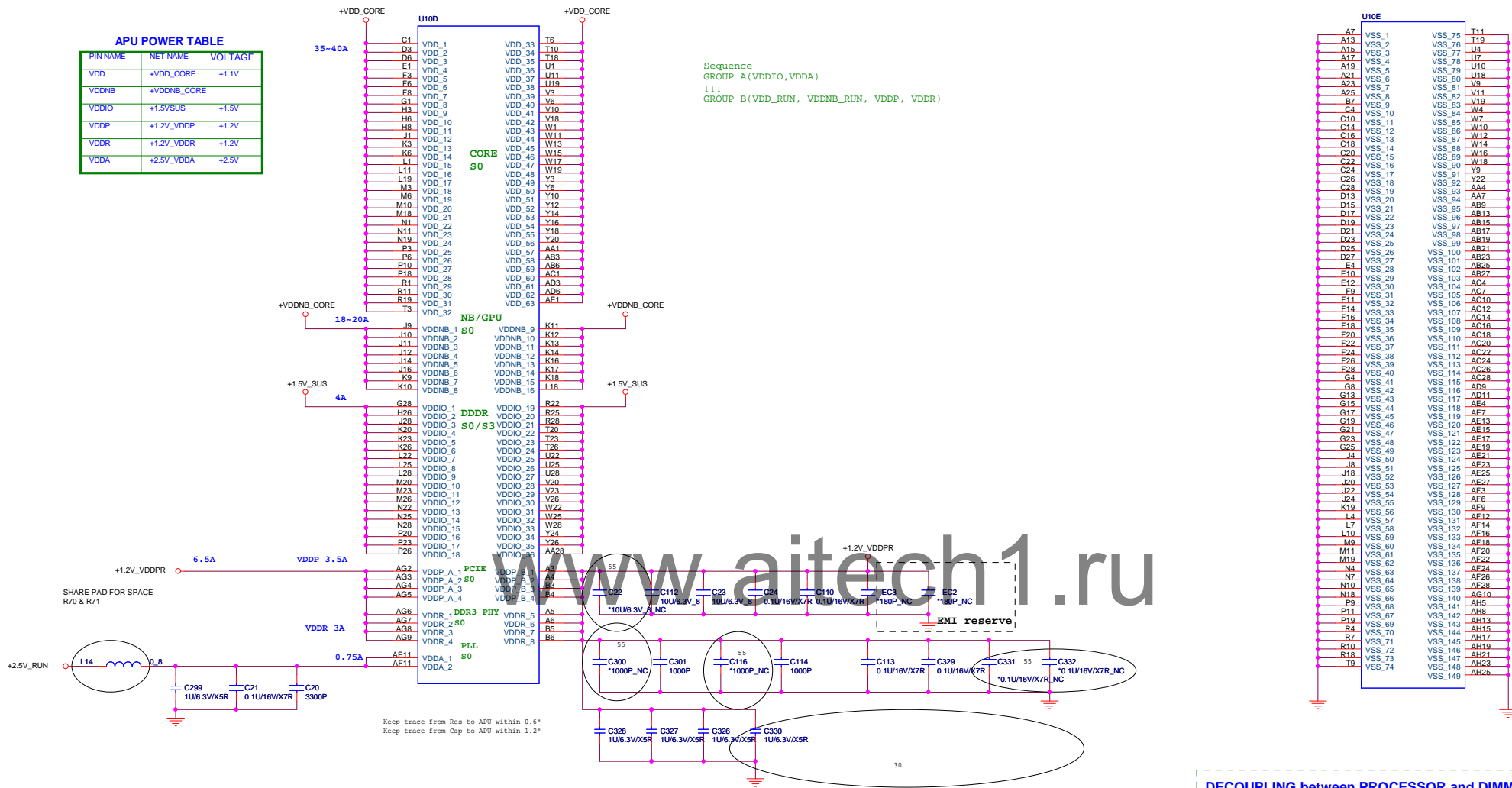
+1.5V_SUS

R100

R60

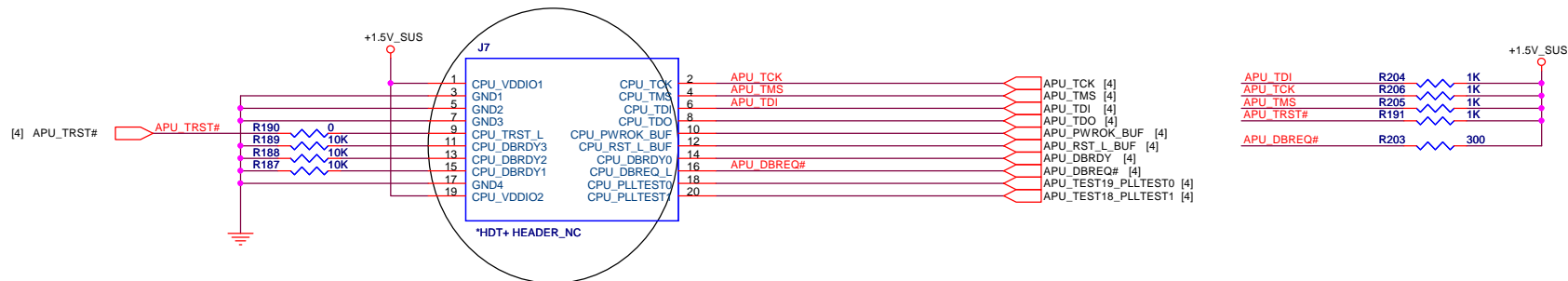


APU POWER TABLE			
PIN NAME	NET NAME	VOLTAGE	
VDD	+VDD_CORE	+1.1V	
VDDNB	+VDDNB_CORE		
VDDIO	+1.5V/SUS	+1.5V	
VDDP	+1.2V_VDDP	+1.2V	
VDDR	+1.2V_VDDR	+1.2V	
VDDA	+2.5V_VDDA	+2.5V	



HDT+ Connector Debug only

Remove after MP



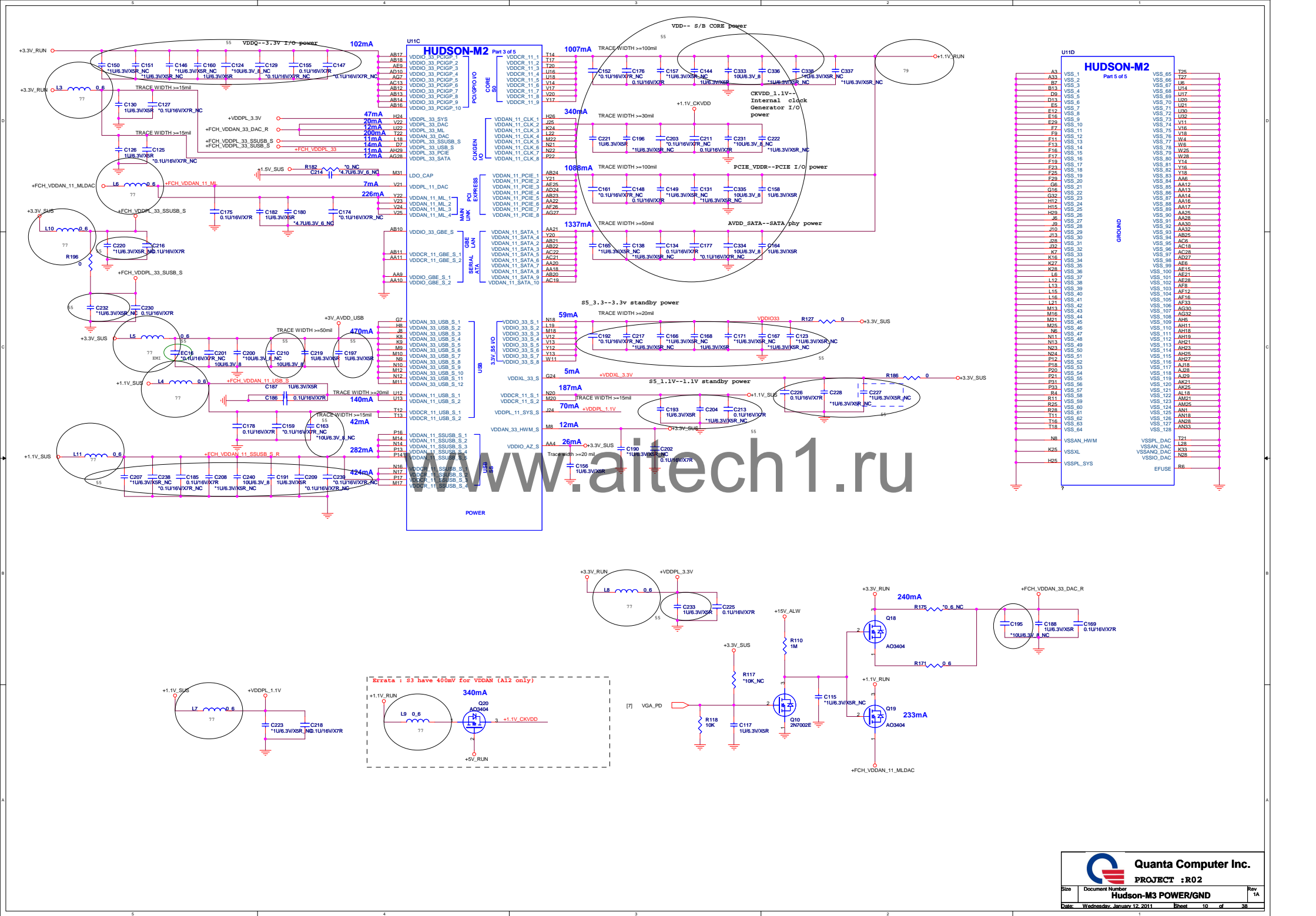
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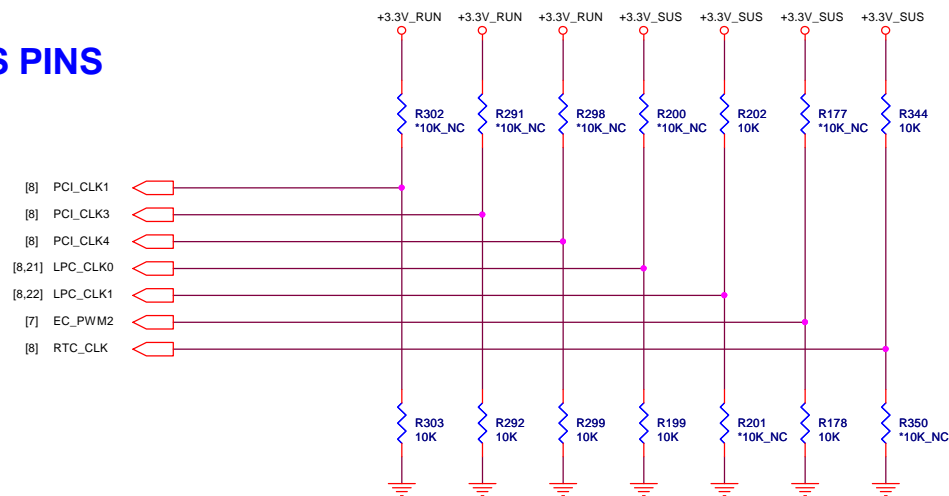
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PROJECT : R02

Size	Document Number	Rev
	Llano DEBUG&OTHER	1A
Date:	Wednesday, January 12, 2011	Sheet 6 of 38



STRAPS PINS

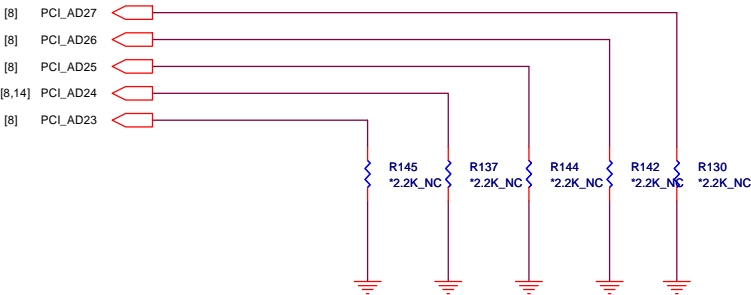


REQUIRED STRAPS

	-----	PCI_CLK1	-----	PCI_CLK3	PCI_CLK4	LPC_CLK0	LPC_CLK1	EC_PWM2	RTC_CLK
PULL HIGH	-----	ALLOW PCIE Gen2	-----	USE DEBUG STRAP	non_Fusion CLOCK MODE	EC ENABLED	CLKGEN ENABLED Setting	LPC ROM	S5 PLUS MODE ENABLED Setting
PULL LOW	-----	FORCE PCIE Gen1 Setting	-----	IGNORE DEBUG STRAP Setting	FUSION CLOCK MODE Setting	EC DISABLED Setting	CLKGEN DISABLED	SPI ROM	S5 PLUS MODE DISABLED

DEBUG STRAPS

FCH HAS 15K INTERNAL PU FOR PCI_AD[27:23]

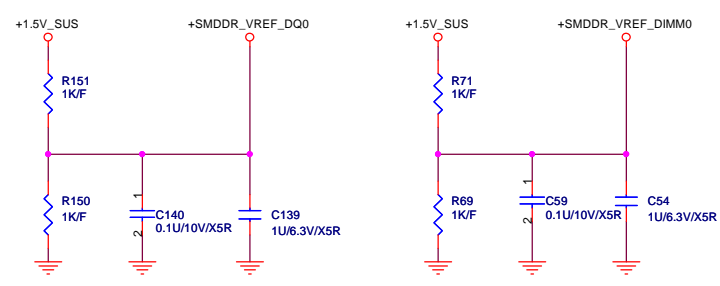
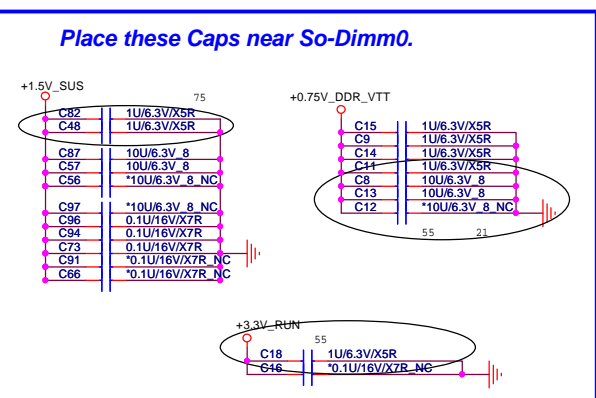
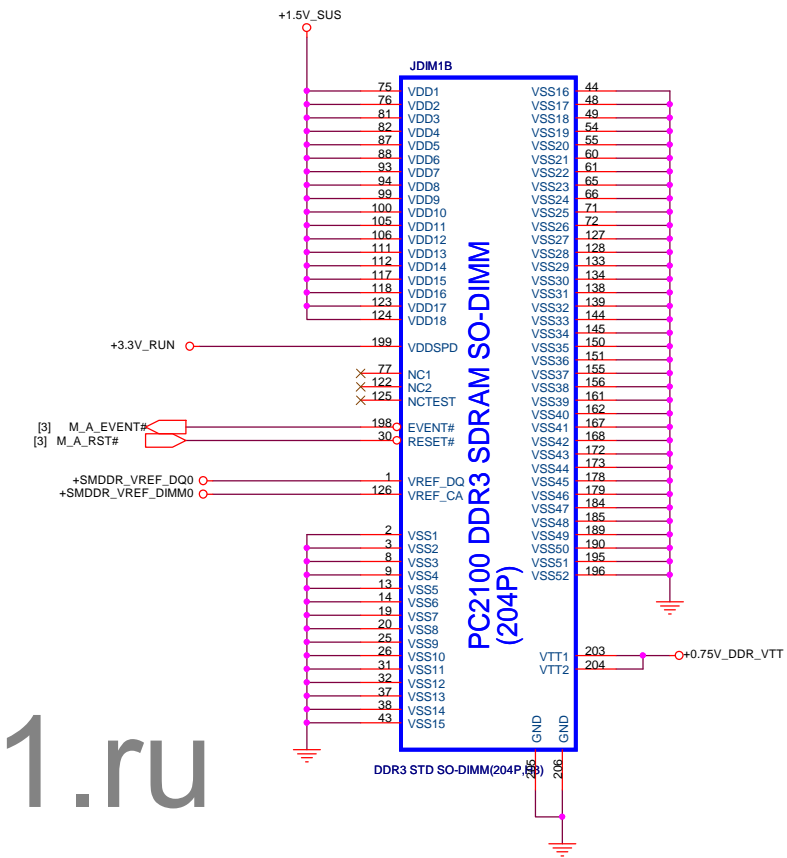
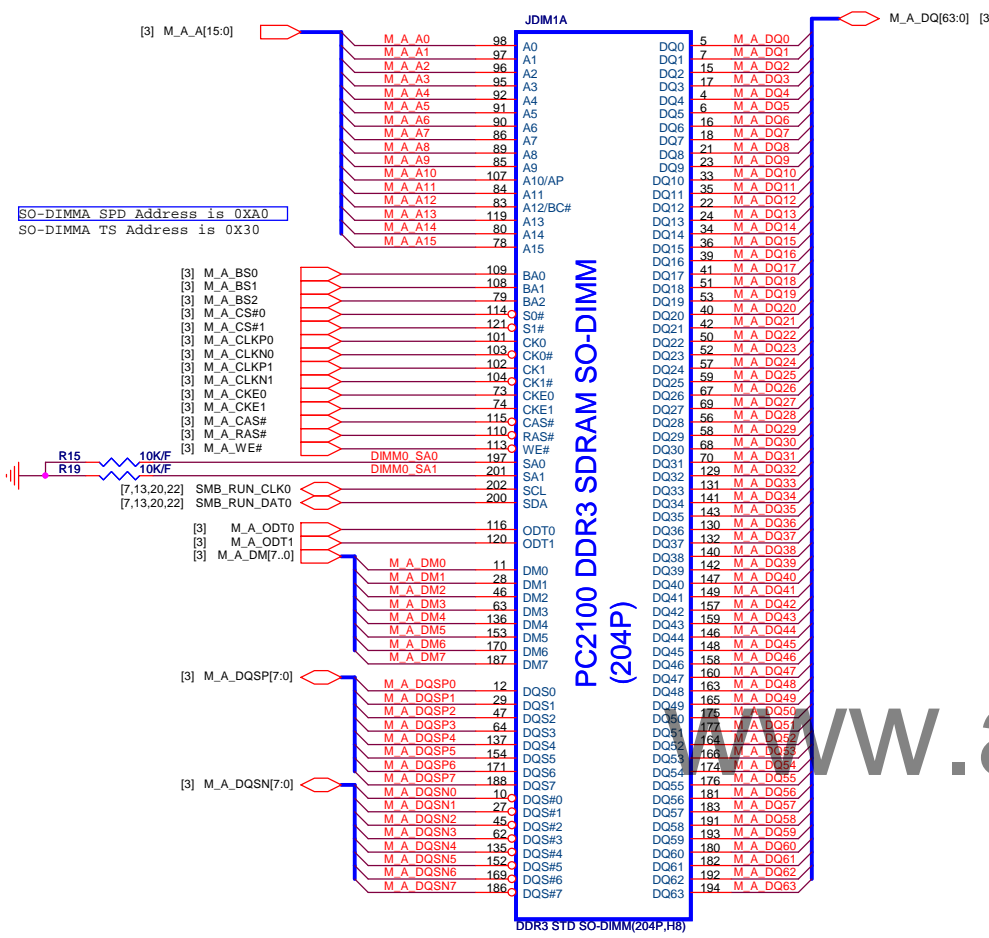


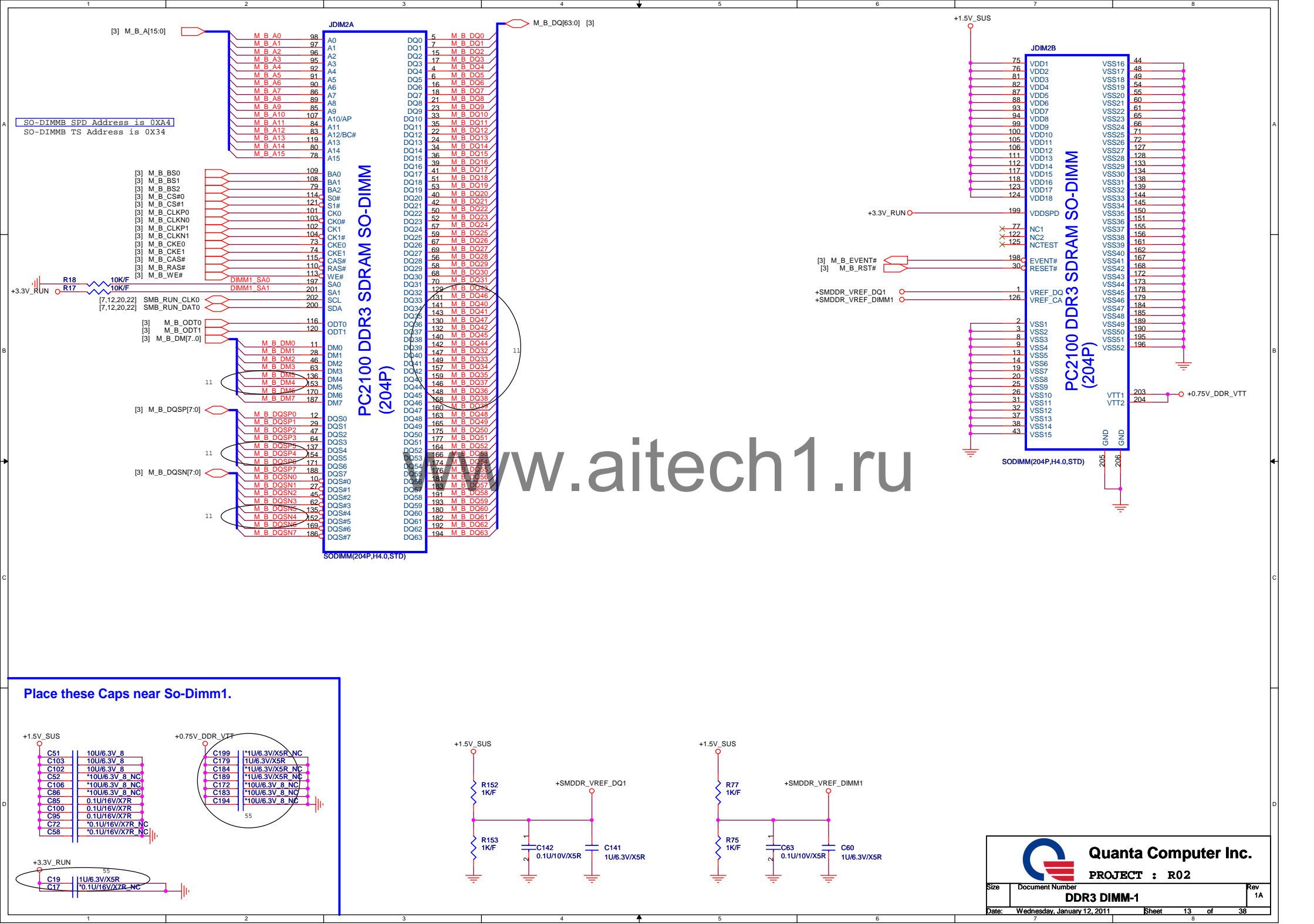
	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE PCI PLL Setting	DISABLE ILA AUTORUN Setting	USE FC PLL Setting	USE DEFAULT PCIE STRAPS Setting	DISABLE PCI MEM BOOT Setting
PULL LOW	BYPASS PCI PLL	ENABLE ILA AUTORUN	BYPASS FC PLL	USE EEPROM PCIE STRAPS	ENABLE PCI MEM BOOT

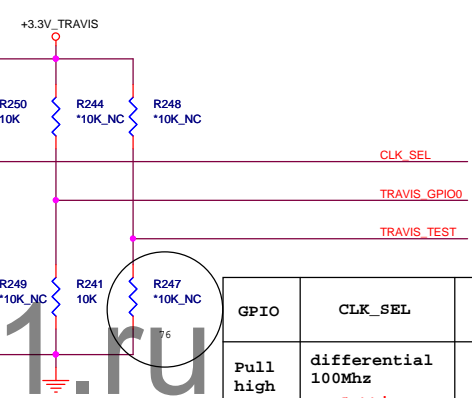
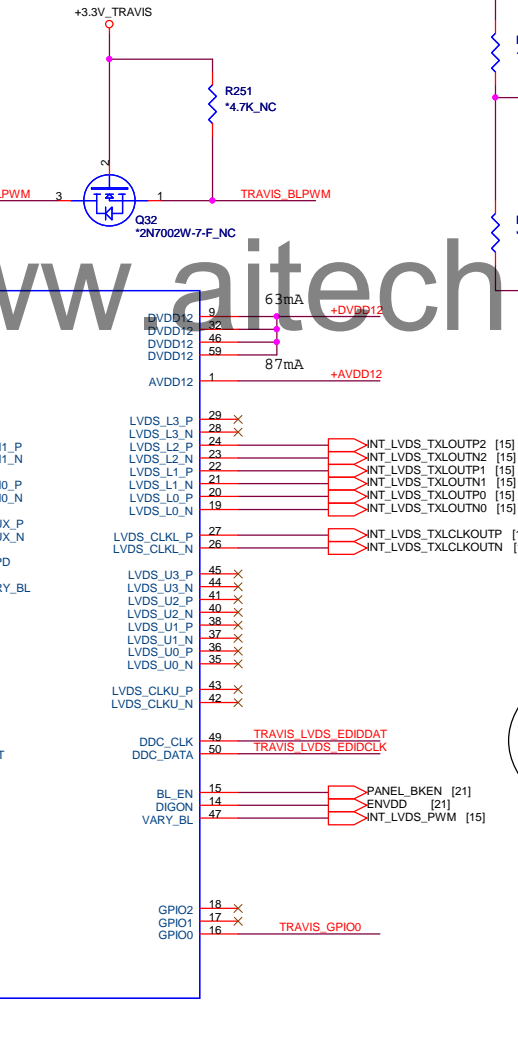
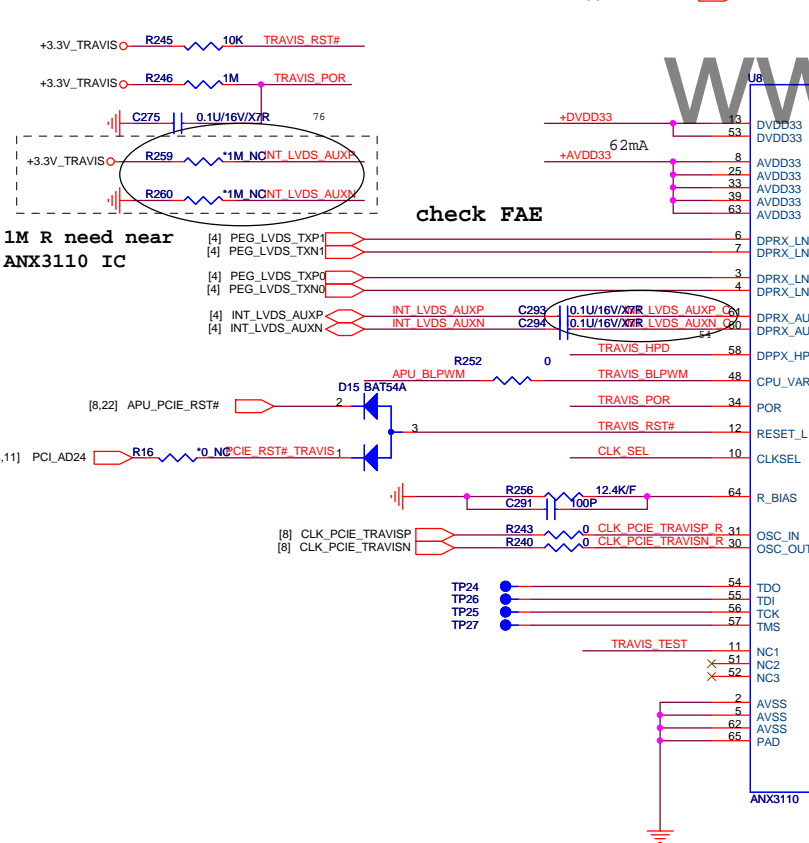
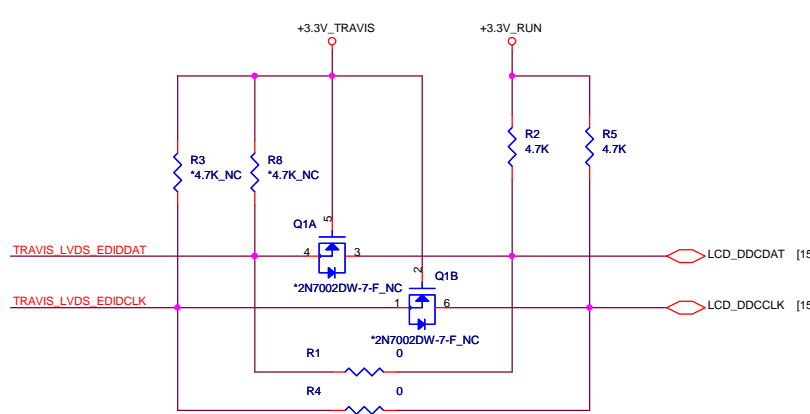
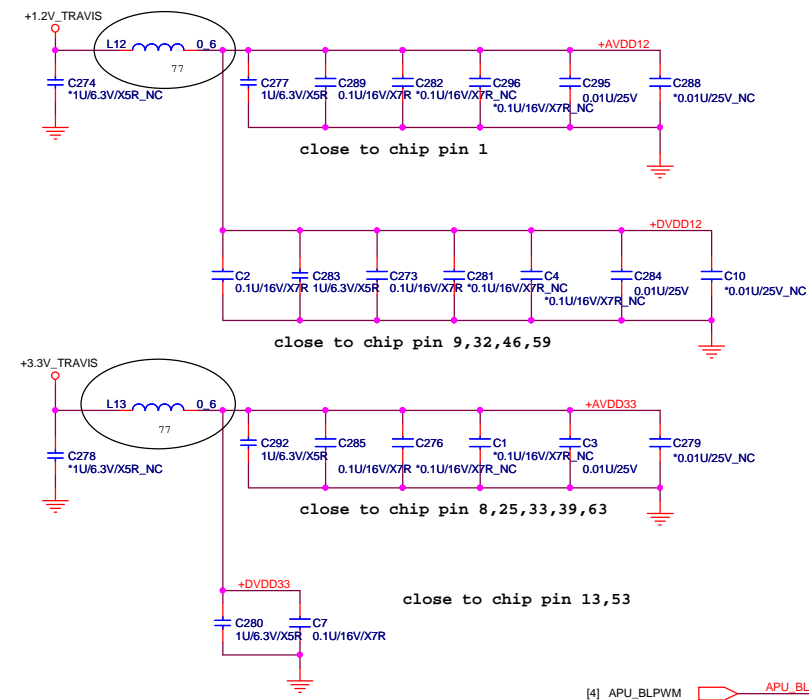


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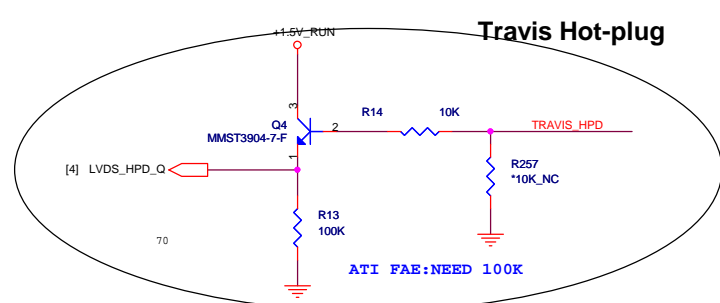
PROJECT : R02

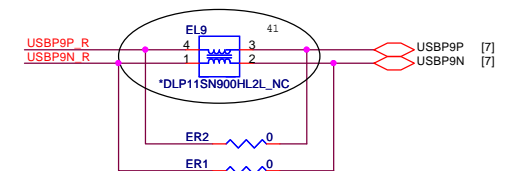
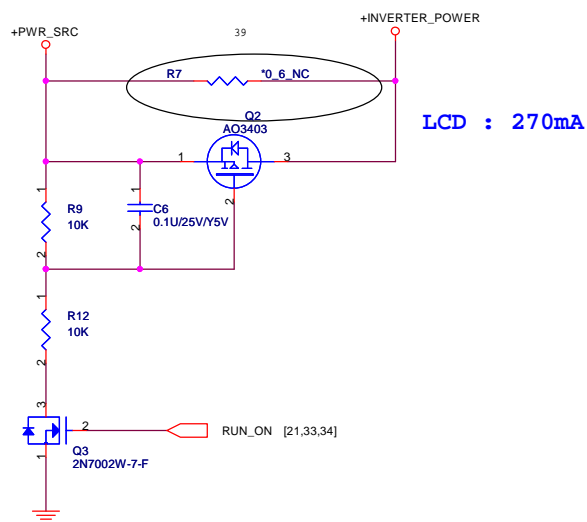
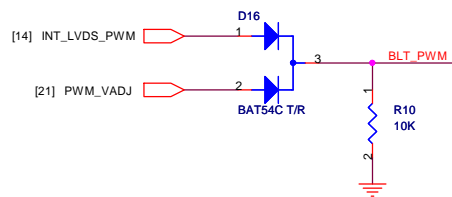
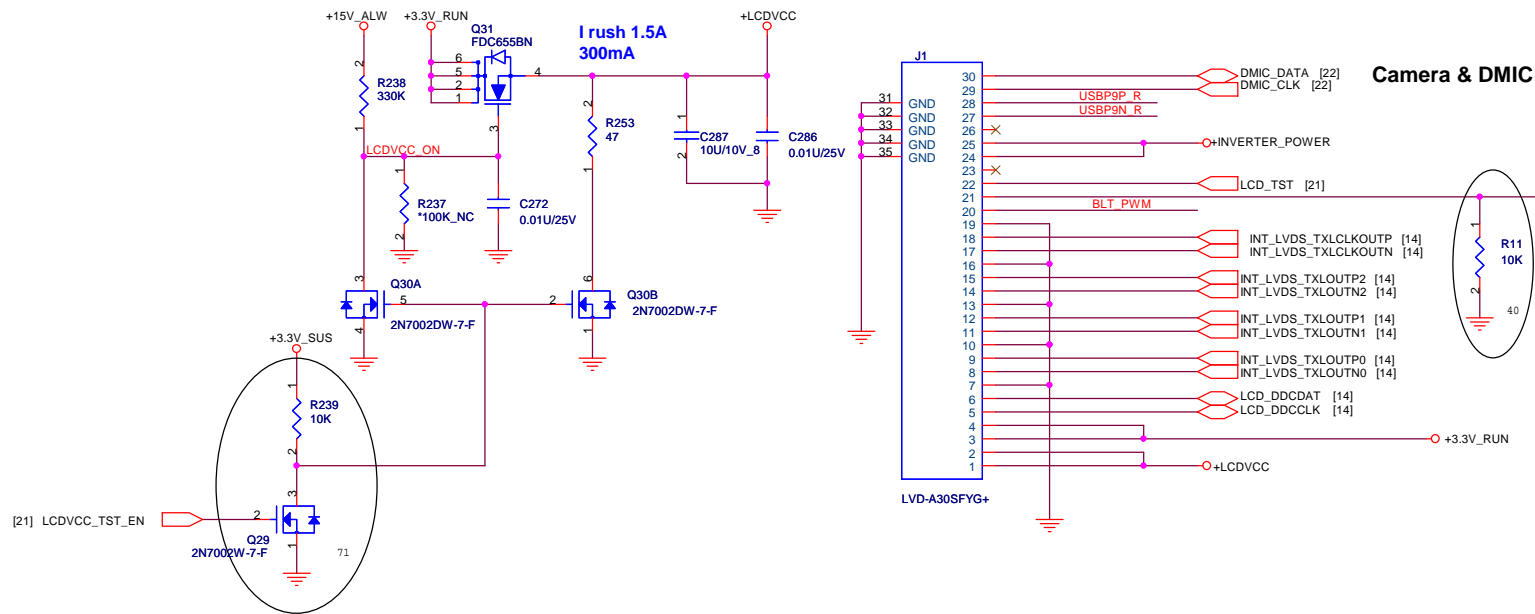




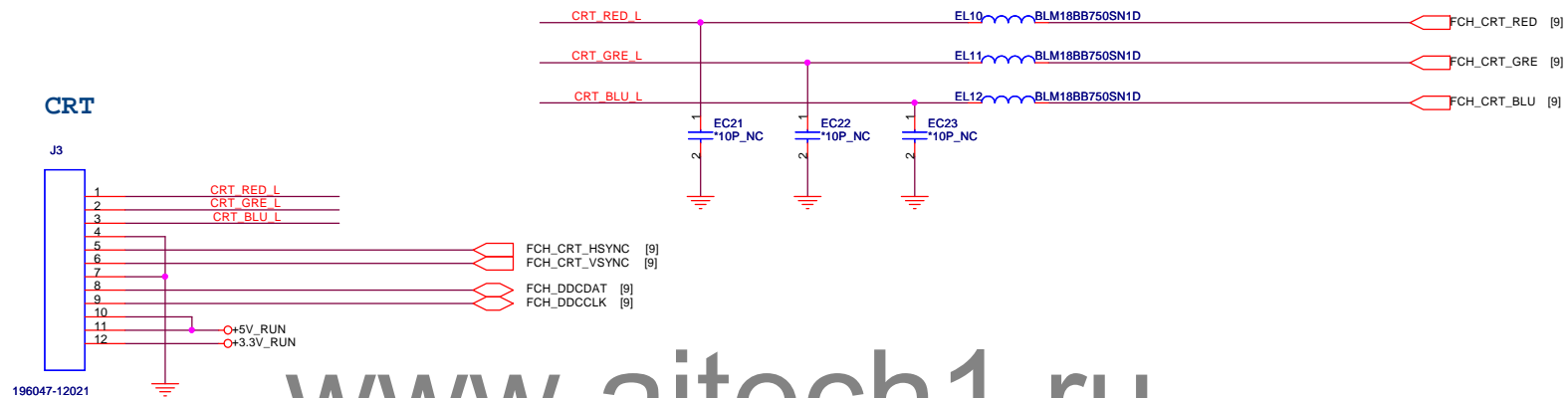


GPIO	CLK_SEL	TRAVIS_GPIO0	TRAVIS_TEST
Pull high	differential 100Mhz Setting	Control by HW	TEST mode enable
Pull low	OSC 27Mhz	Control by SW Setting	TEST mode disable Setting





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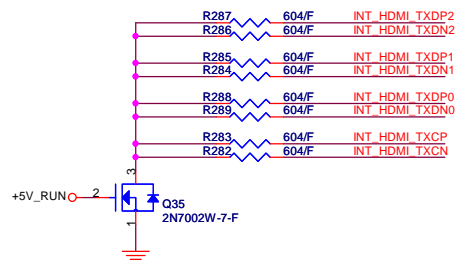
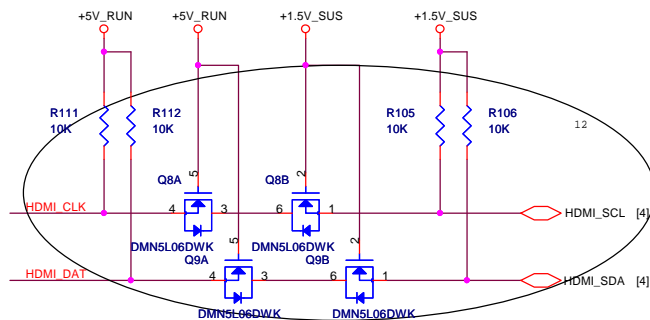


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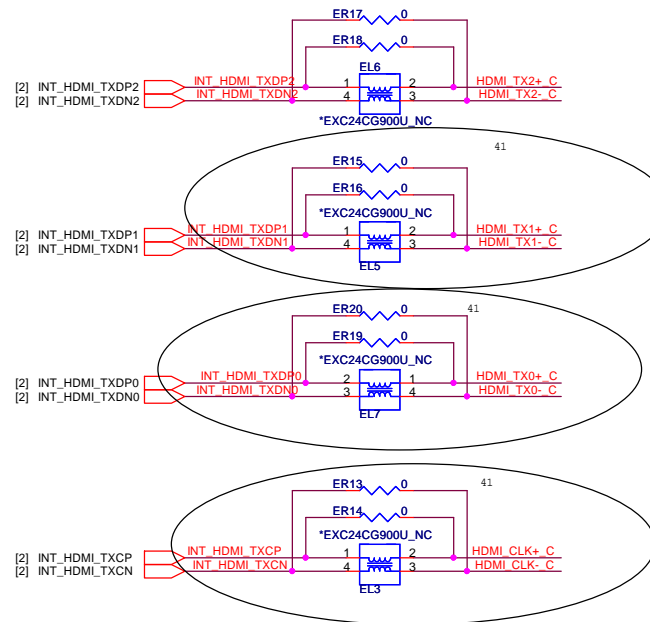
PROJECT : R02

Size	Document Number	Rev
	CRT CONN.	1A
Date:	Wednesday, January 12, 2011	Sheet 16 of 38

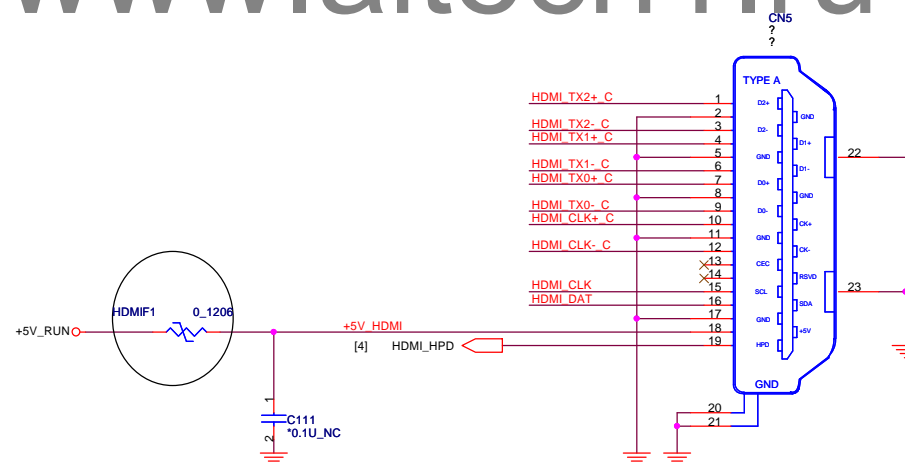
UMA HDMI



Reserve for EMI and close to HDMI CONN

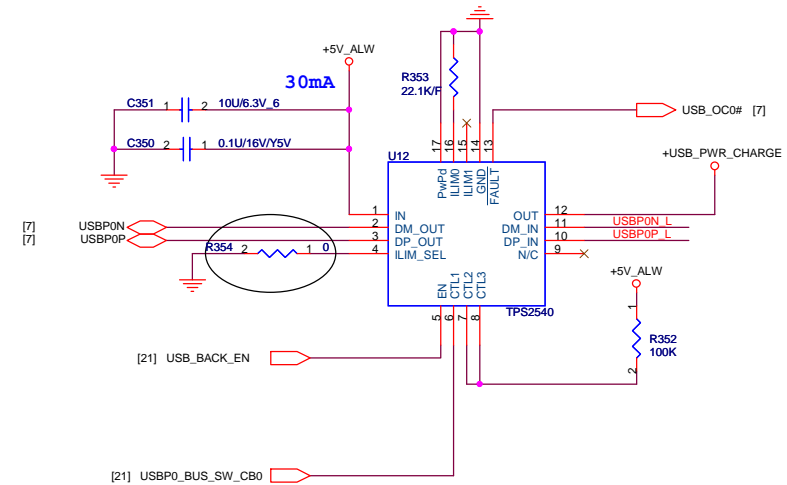


www.aitech1.ru HDMI Conn.



S3/S5 USB charging circuit

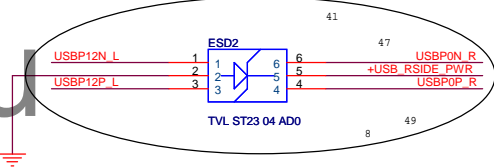
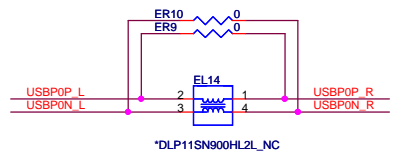
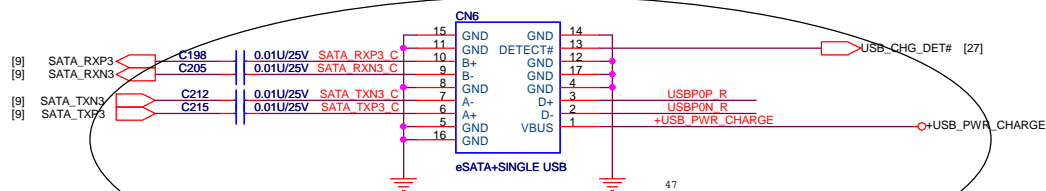
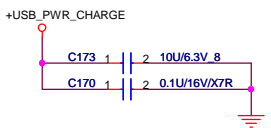
USB3.0 + USB2.0 + ESATA + USB Conn + Power share



USBP0_BUS_SW_CB0	Mode
Low	DCP, Auto-detect
High	CDP, BC Spec 1.1

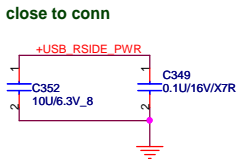
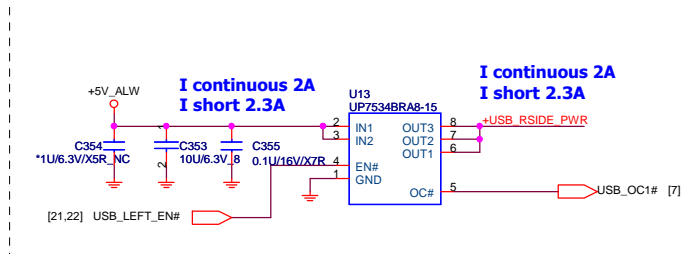
	R353	mA
OC limitation	100k ohm	480
	22.1k ohm	2171

Applied Now

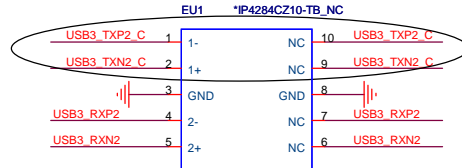
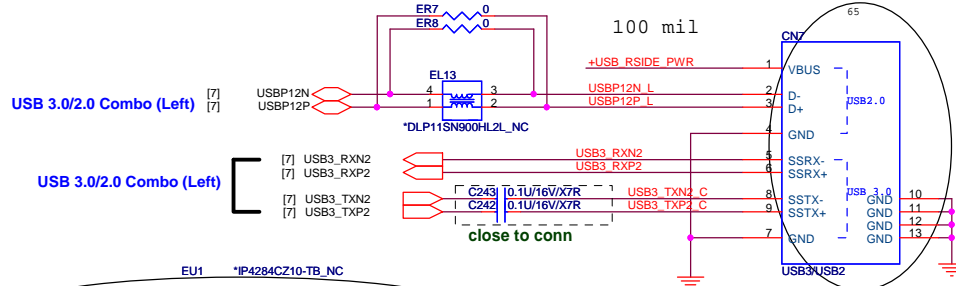


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UPI power switch

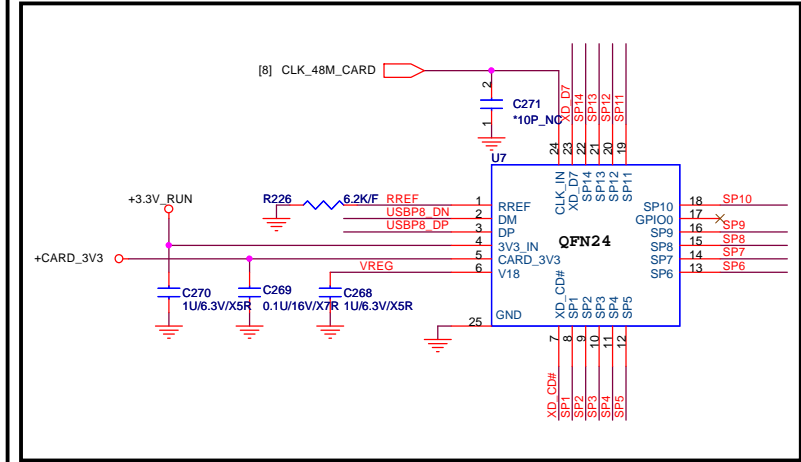
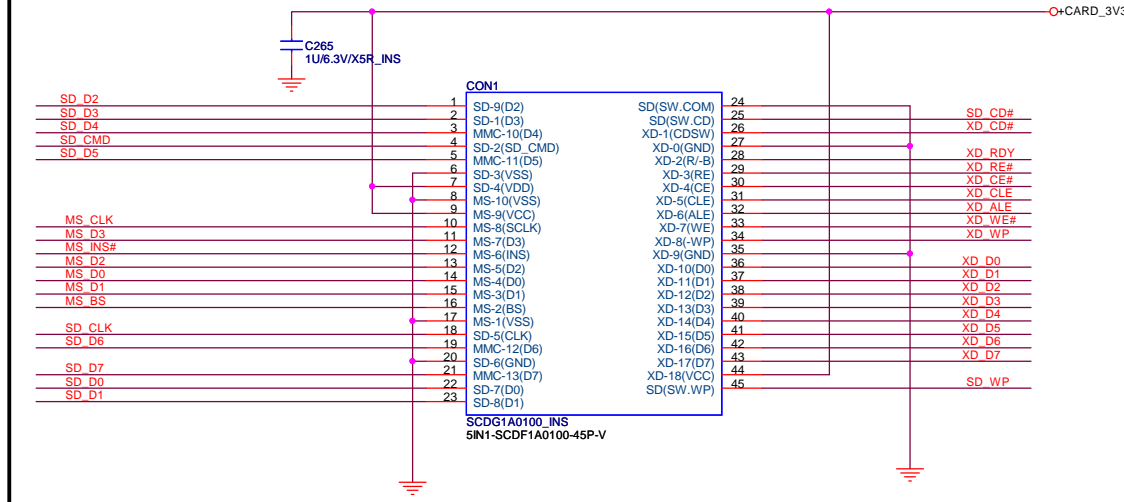


USB3.0 + USB2.0 + USB Conn + Power share



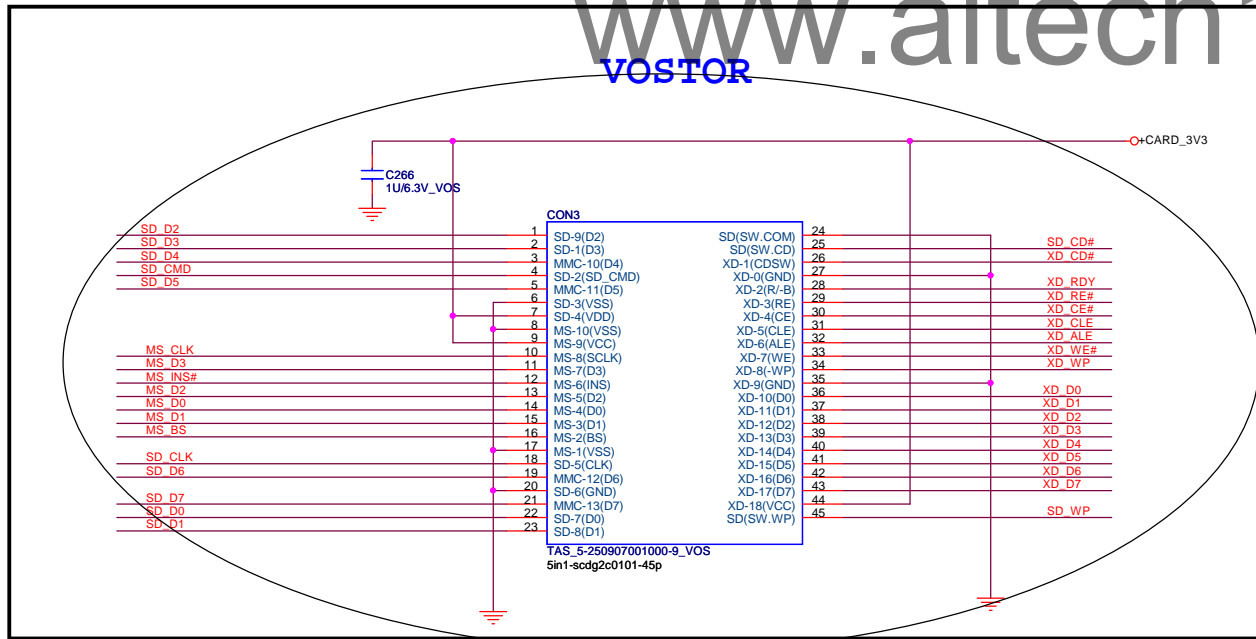
Platforms should put in PADS for the USB chokes if they have the room. Chokes should be NOPOP.

Inspiron



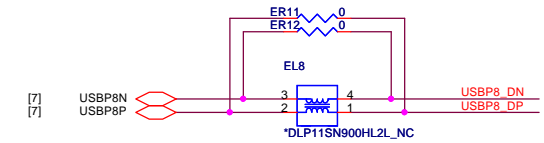
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VOSTOR



SP1	XD RDY	SD WP	MS CLK
SP2	XD RE#	SD D1	MS INS#
SP3	XD CE#	SD D0	MS D7
SP4	XD CLE	SD D7	MS D3
SP5	XD ALE	SD CD#	MS D6
SP6	XD WE#	SD D6	MS D2
SP7	XD WP	SD CLK	MS D0
SP8	XD D0	SD D5	MS D0
SP9	XD D1	SD D4	MS D4
SP10	XD D2	SD D3	MS D1
SP11	XD D3	SD D2	MS D5
SP12	XD D4	SD D2	MS D5
SP13	XD D5	SD D2	MS D5
SP14	XD D6	SD D2	MS D5

Share Pin

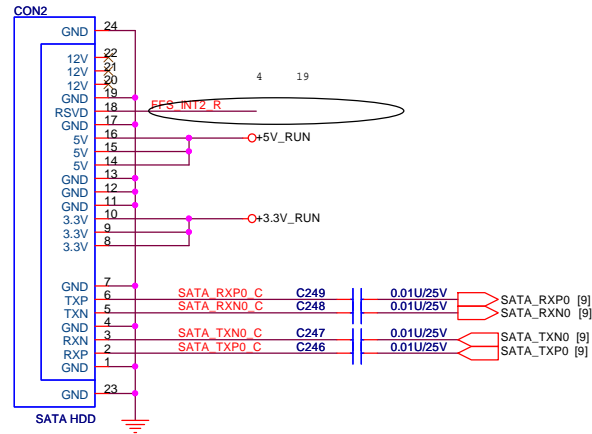


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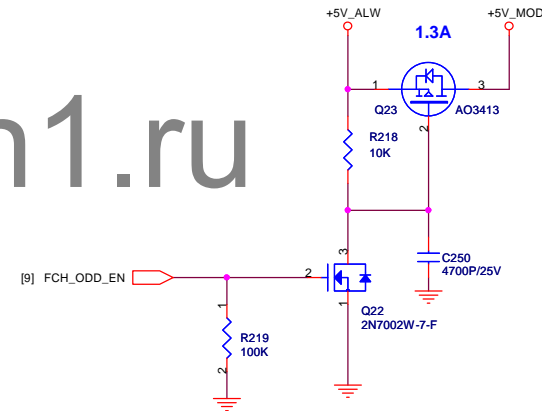
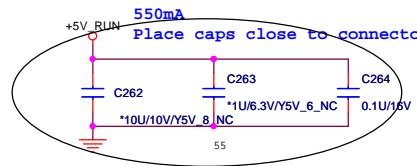
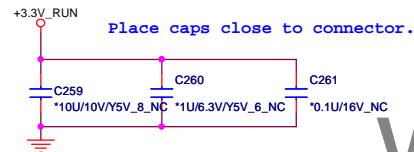
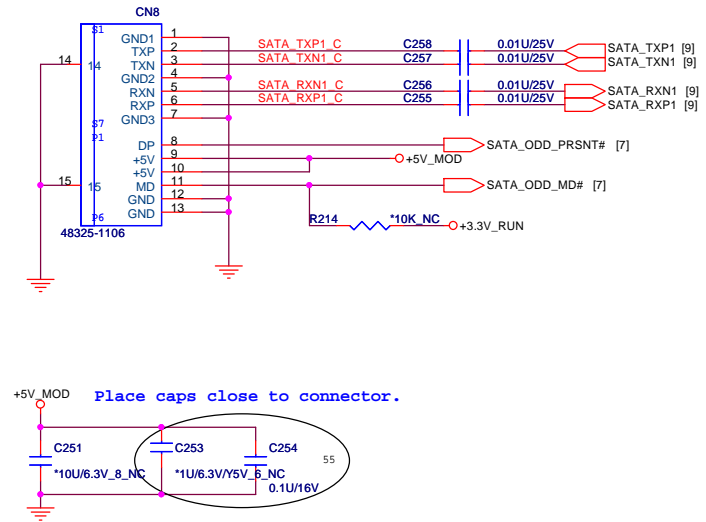
PROJECT : R02

Size	Document Number	Rev
	Cardreader (RTS5128)	1A
Date	Wednesday, January 12, 2011	Sheet 19 of 38

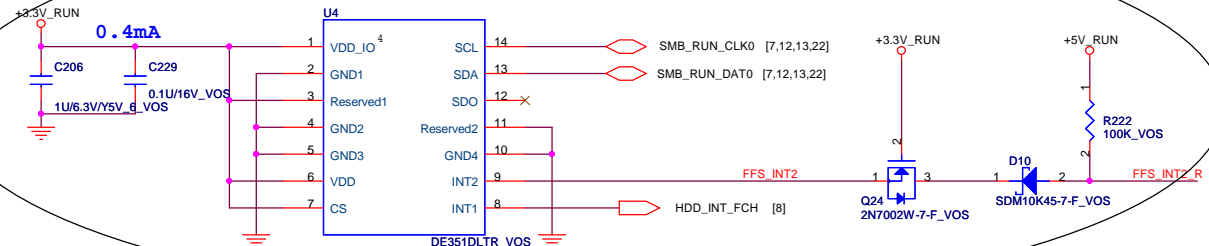
SATA Connector

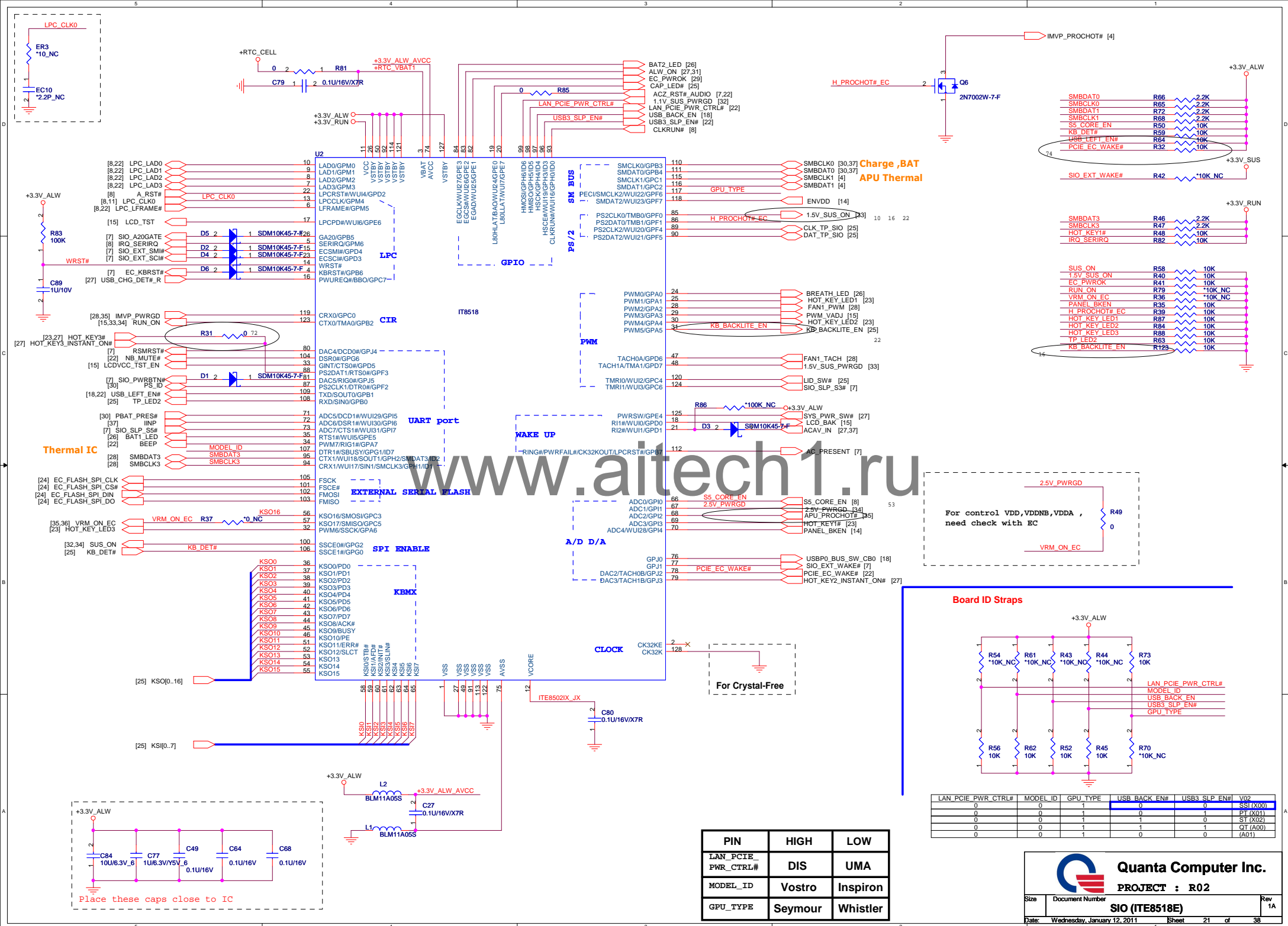


ODD Connector

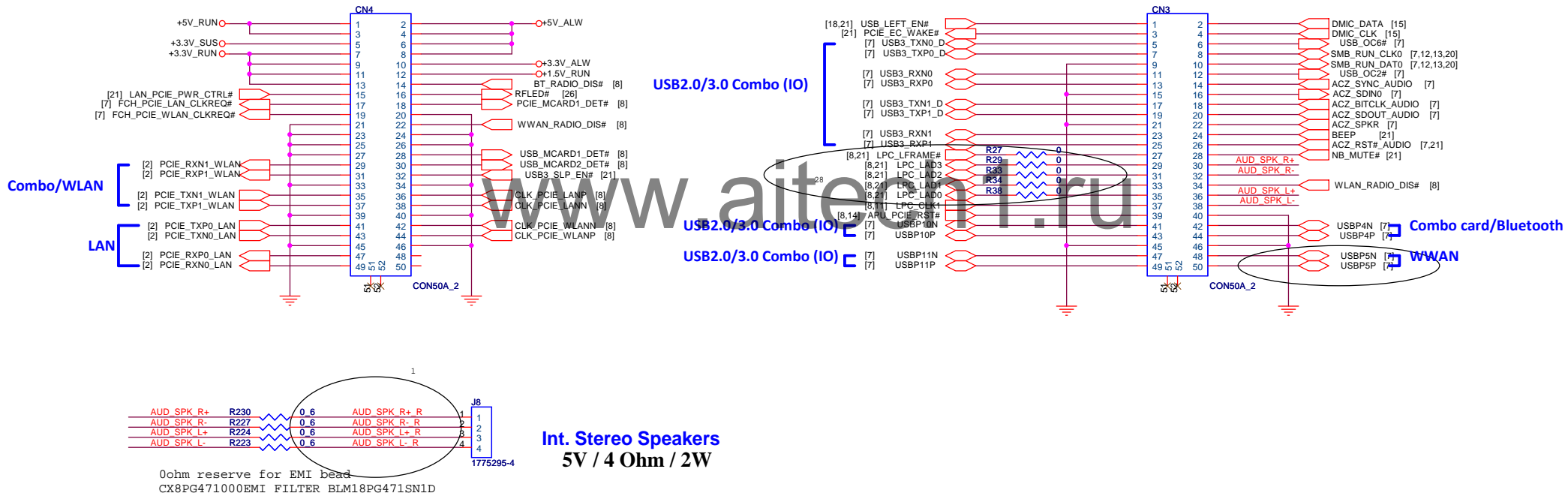


3-axis Fall Sensor (HDD data protector)

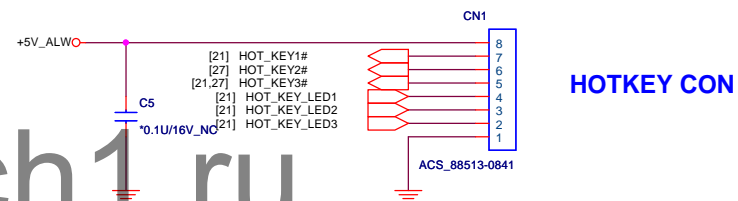




+5V_RUN for Codec :PVDD TYP=600mA AVDD TYP=40mA	+3.3V_RUN for WLAN :TYP=600mA for WWAN MAX=2500mA Codec :DVDD=22.8mA	+5V_ALW for USB power switch 2000mA x2 =4000mA
+3.3V_SUS for Codec :DVDD I-O=22.8mA LAN :lcc33(3.3V) ,typ=70mA lcc10(1.05V) ,typ=300mA	+1.5V_RUN for WLAN MAX =500mA for WWAN MAX =500mA	+3.3V_ALW for LAN :lcc33(3.3V) ,typ=70mA lcc10(1.05V) ,typ=300mA



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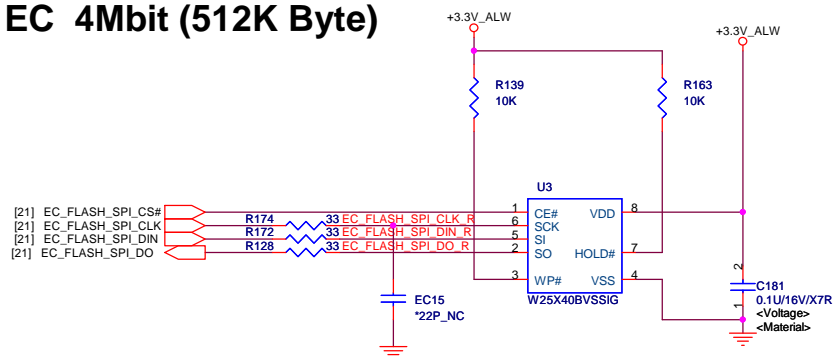


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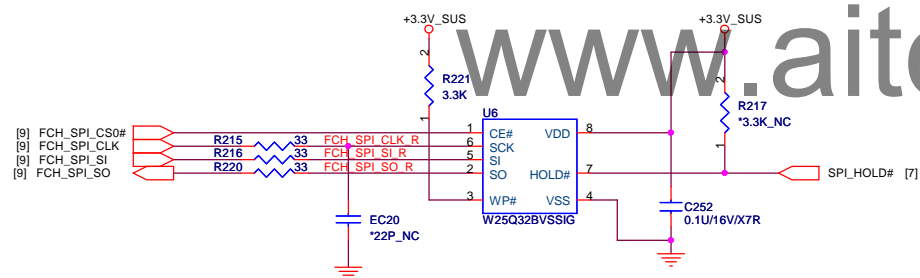
PROJECT : R02

Size	Document Number	Rev
	HOTKEY CON	1A
Date:	Wednesday, January 12, 2011	Sheet 23 of 38

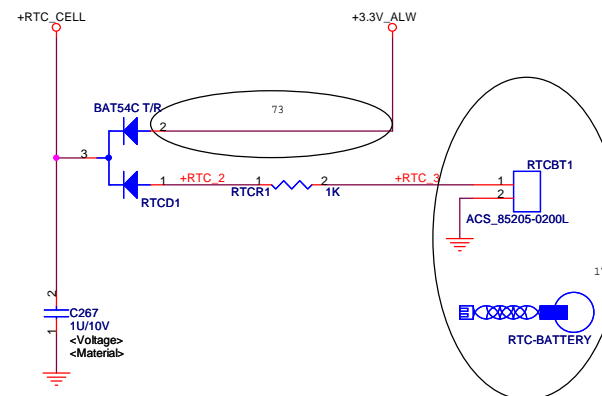
For EC 4Mbit (512K Byte)



For FCH 32Mbit (4M Byte)



RTC



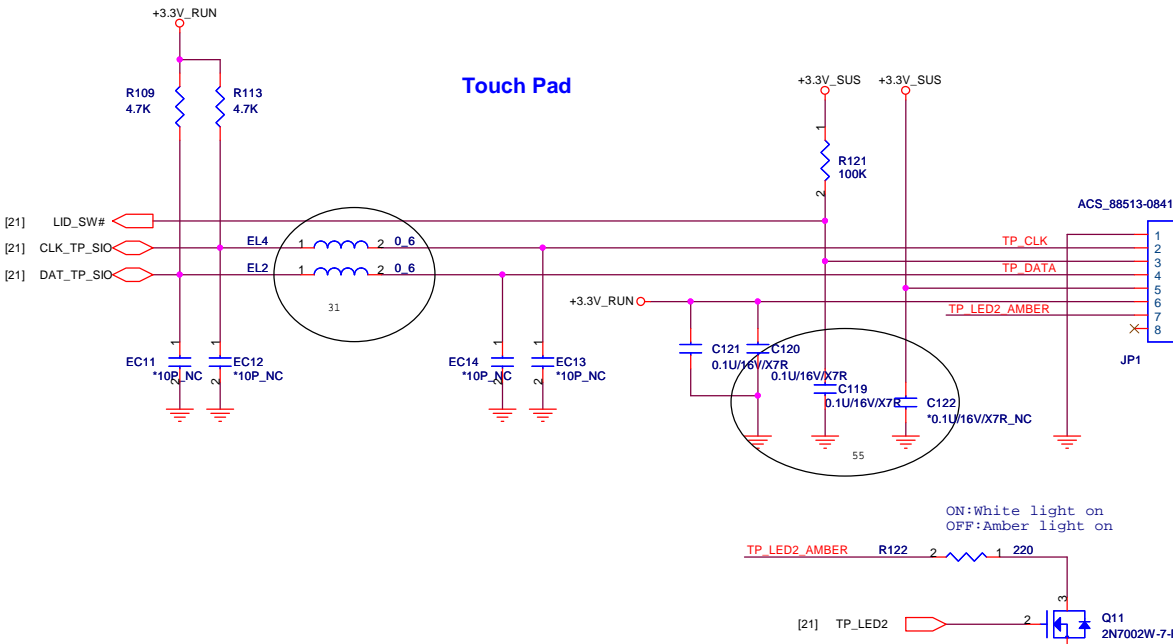
Double, 25°C, Vf=0.4V, If=25mA
one, 25°C, Vf=0.35V, If=15.8mA



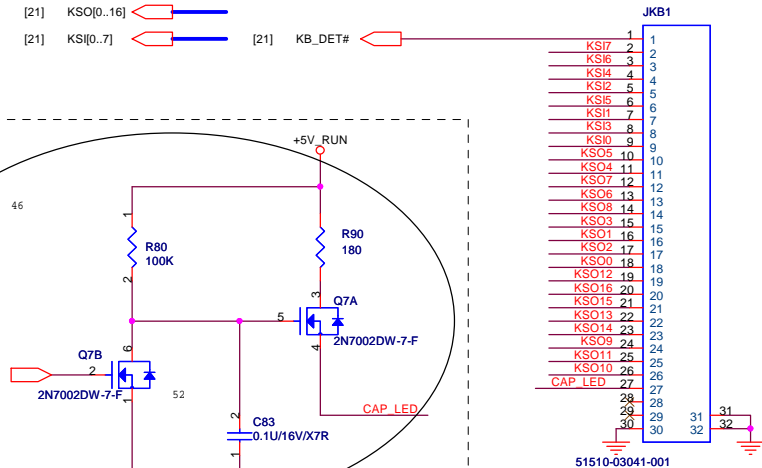
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PROJECT : R02

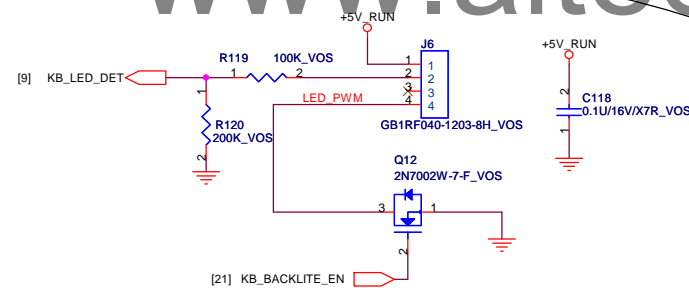
Size	Document Number	Rev
	FLASH / RTC	1A
Date:	Wednesday, January 12, 2011	Sheet 24 of 38



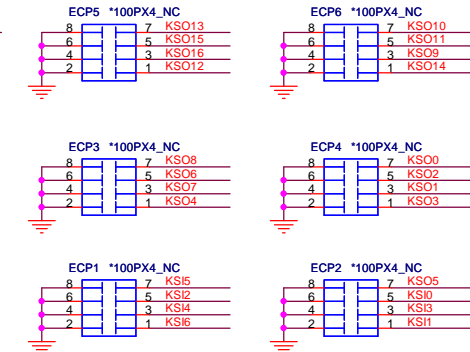
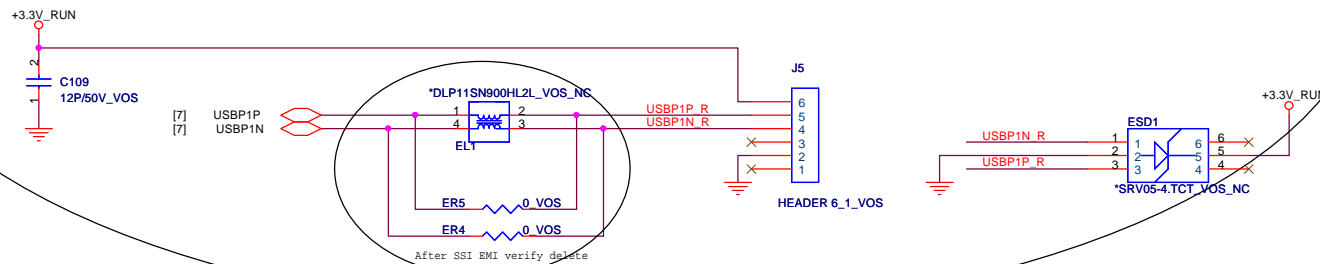
KEYBOARD CONNECTOR



Key board illumination Vostro only



Biometric Vostro only



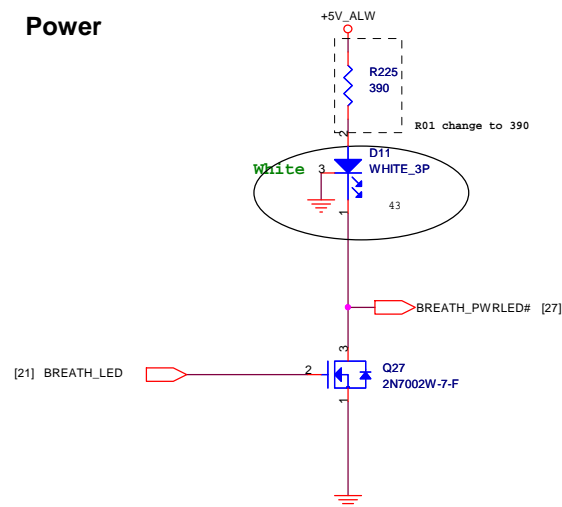
Layout Note: 100P CAPS CLOSE TO CONN.



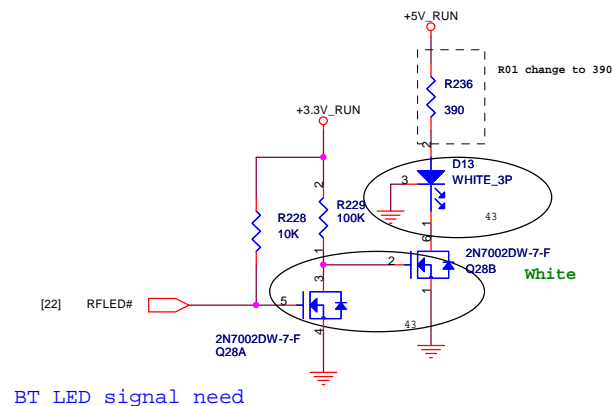
Quanta Computer Inc.

PROJECT : R02

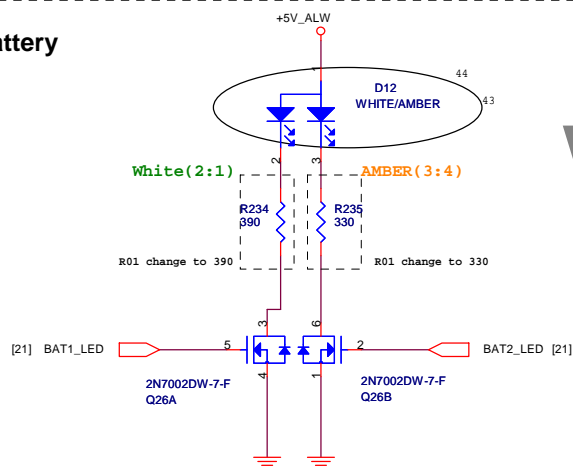
Power



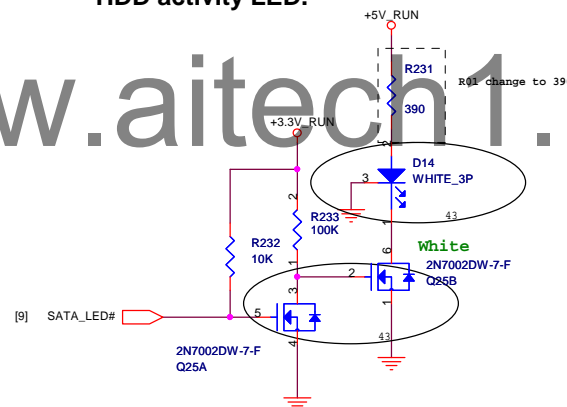
Bluetooth / WLAN on/off LED



Battery



HDD activity LED.



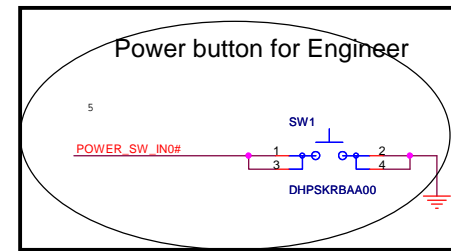
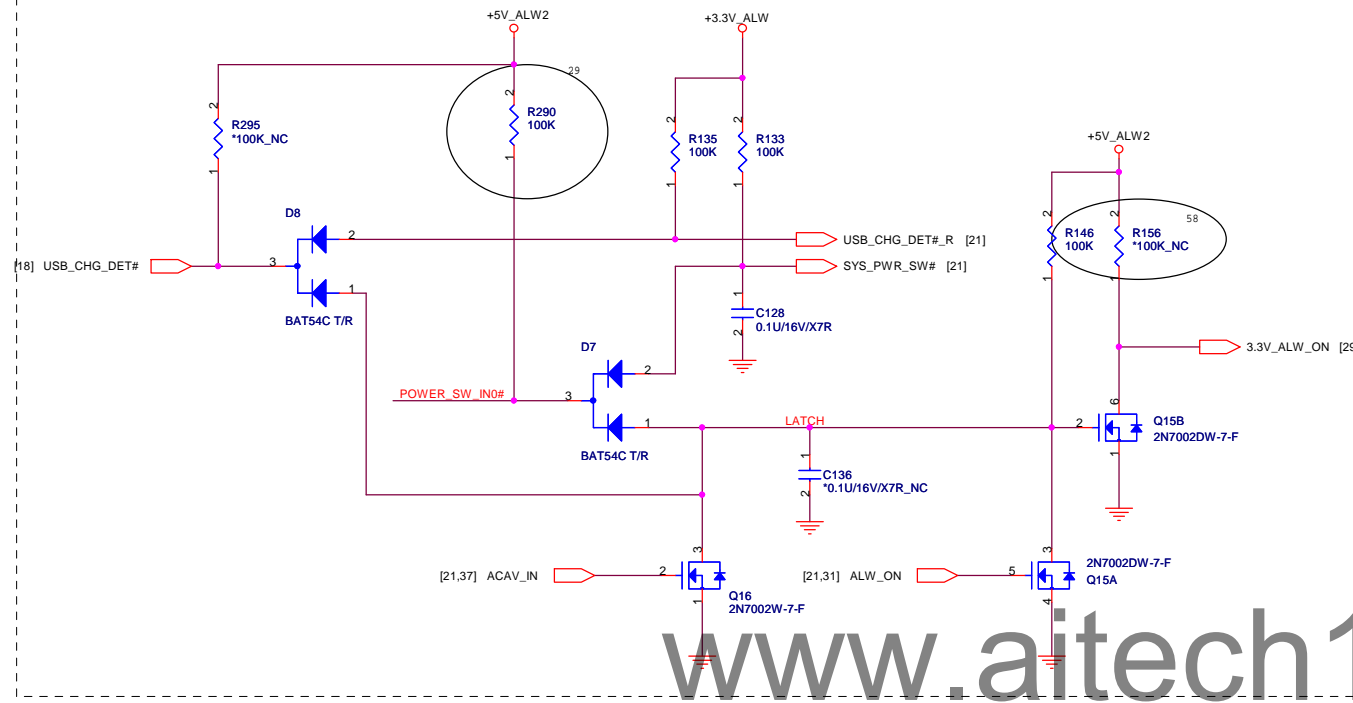
VOSTOR	R225, R231, R234, R236	R234
	180 ohm PN:CS11802JB15	240ohm PN:CS12402JB13
Inspiron	R225, R231, R234, R236	R234
	390 ohm PN:CS13902JB14	330 ohm PN:CS13302JB21



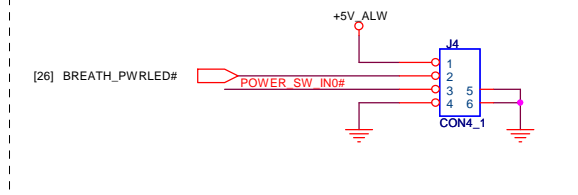
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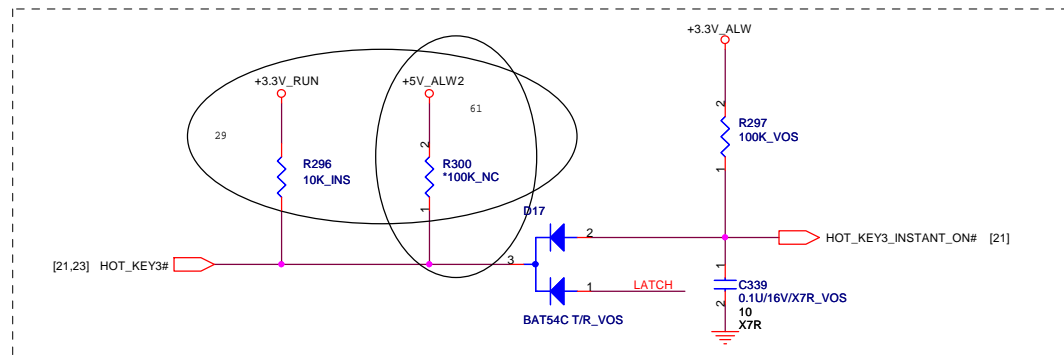
3VALW ON POWER LOGIC



PWR button board



Instant ON function



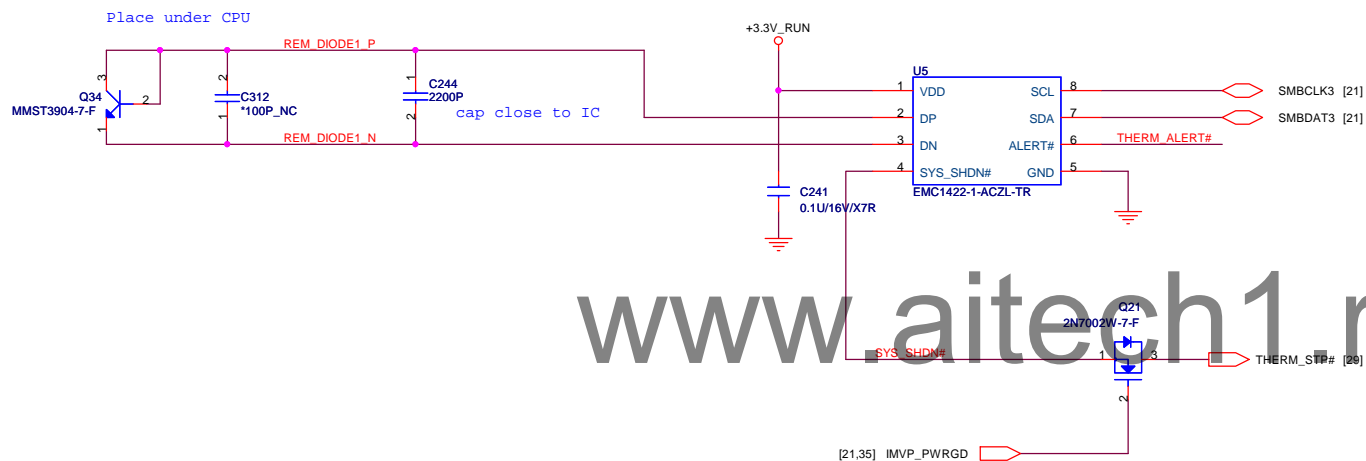
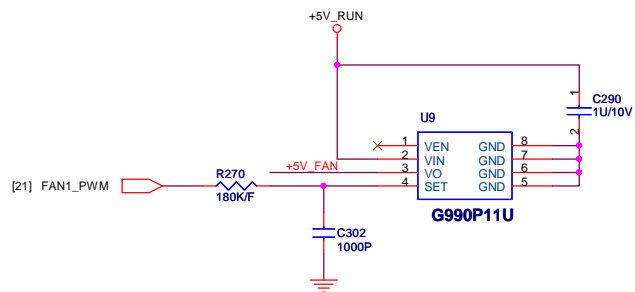
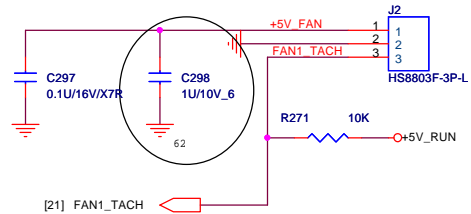
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PWR SW/LED

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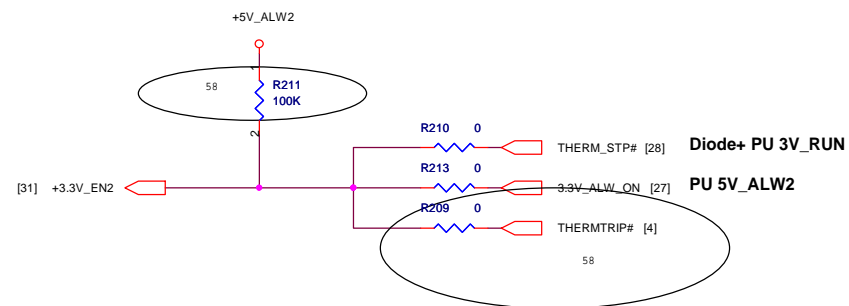
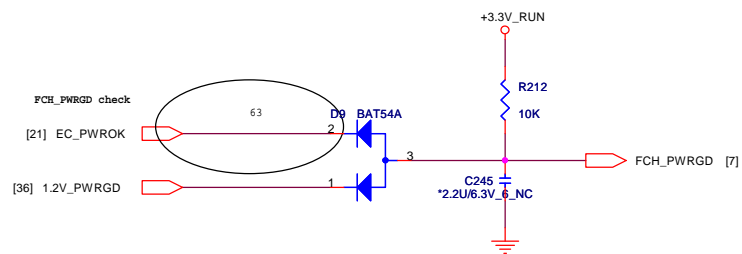


SYS_SHD# \ ALERT#	4.7K	6.8K	10K	15K	22K	33K
4.7K	77 'C	83 'C	89 'C	95 'C	101 'C	107 'C
6.8K	78 'C	84 'C	90 'C	96 'C	102 'C	108 'C
10K	79 'C	85 'C	91 'C	97 'C	103 'C	109 'C
15K	80 'C	86 'C	92 'C	98 'C	104 'C	110 'C
22K	81 'C	87 'C	93 'C	99 'C	105 'C	111 'C
33K	82 'C	88 'C	94 'C	100 'C	106 'C	112 'C

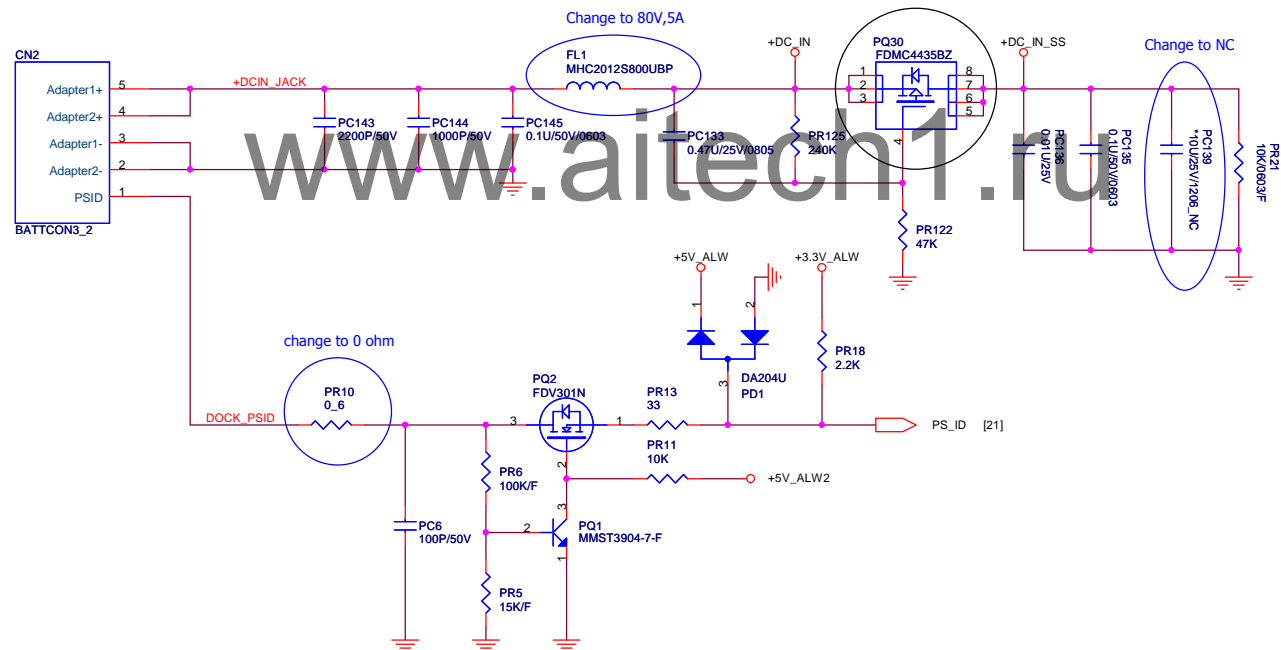
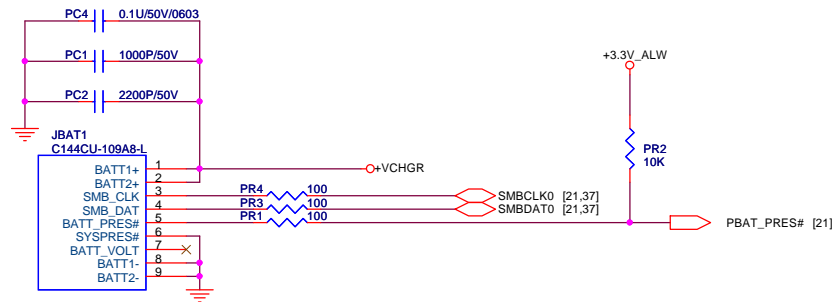


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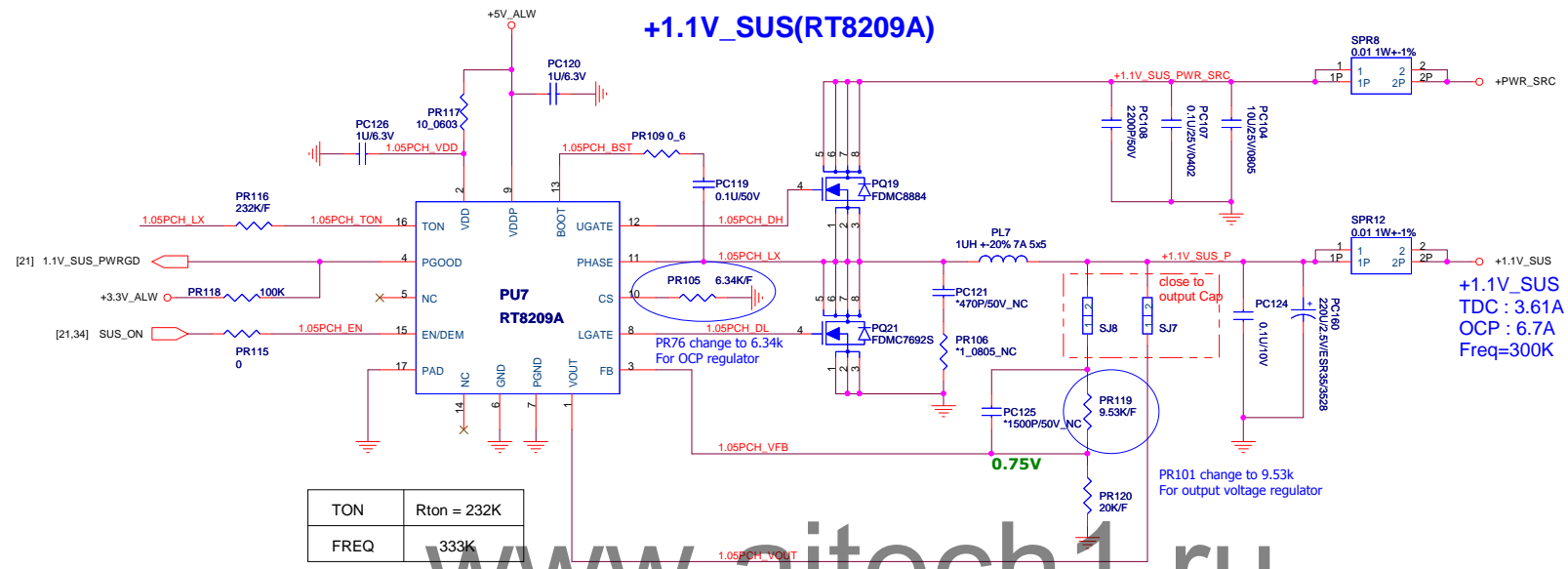
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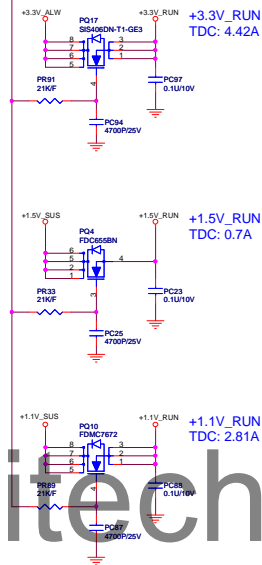
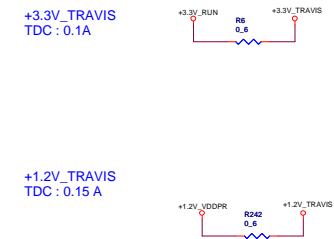
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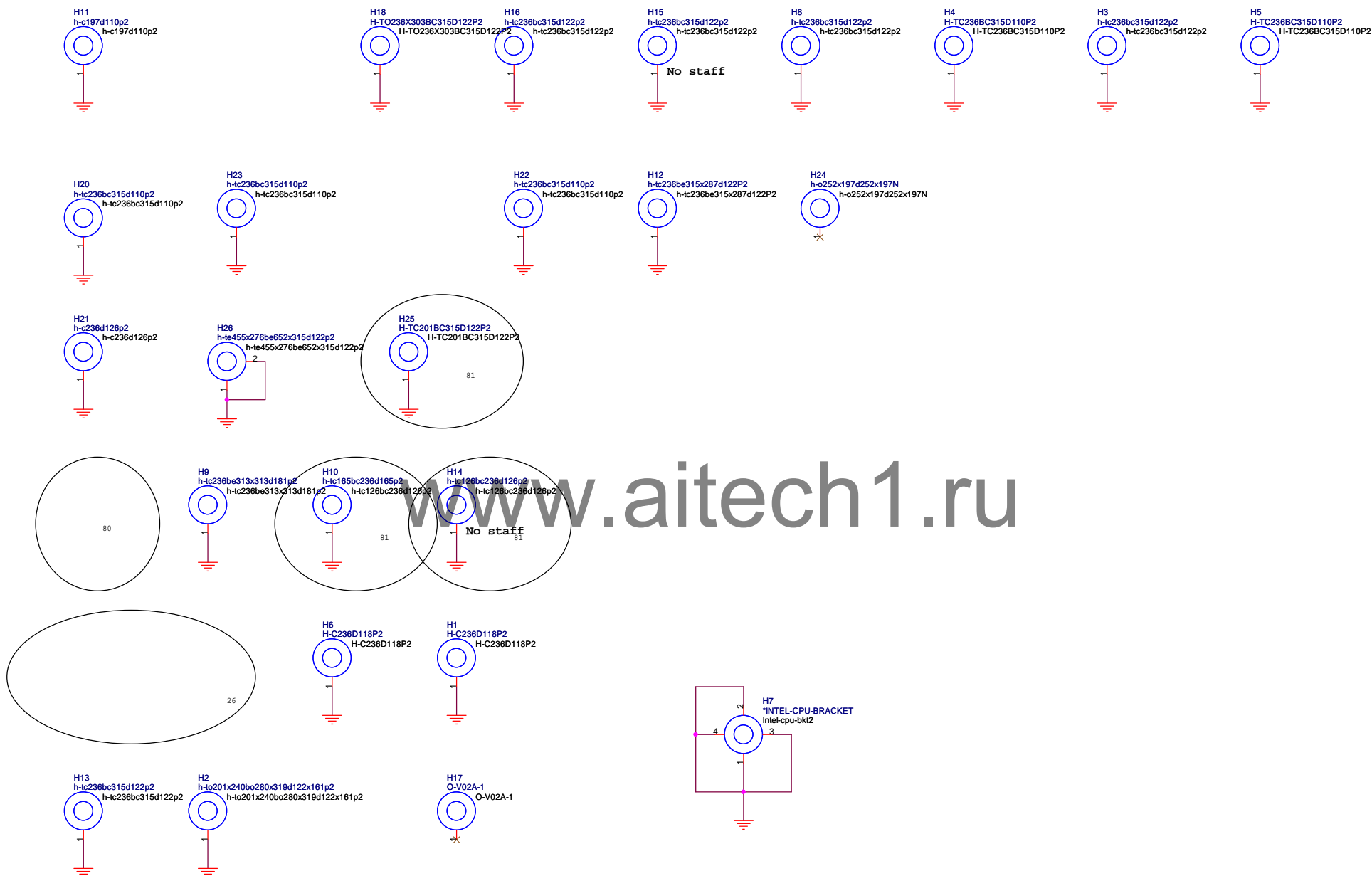
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